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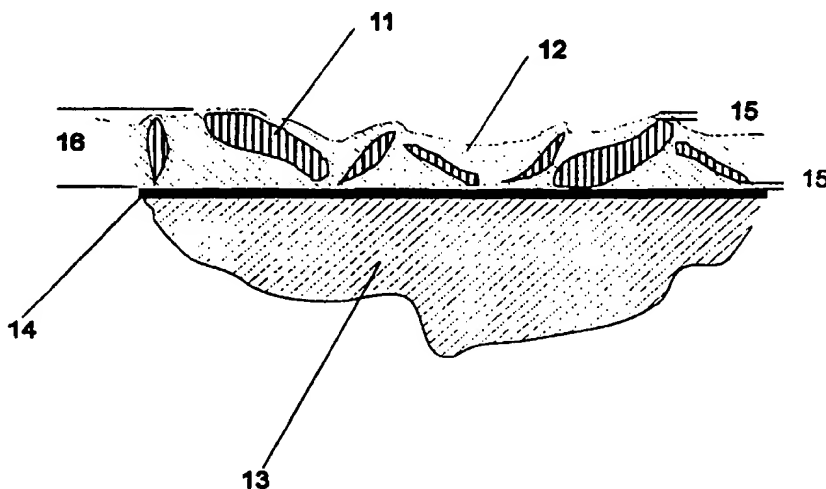
(71)(72) Applicants and Inventors: TUCK, Richard, Allan [GB/GB]; 34 Park Lane, Slough SL3 7PF (GB). LATHAM, Rodney, Vaughan [GB/GB]; 18 Chantry Road, Mosely, Birmingham B13 8DH (GB). TAYLOR, William [GB/GB]; 6 Elm Grove, Hartlepool, Cleveland TS26 8LZ (GB).

(74) Agent: STANLEY, David, William; The Innovation Centre, University Road, Heslington, York YO1 5DG (GB).

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(54) Title: FIELD ELECTRON EMISSION MATERIALS AND DEVICES

**(57) Abstract**

A field electron emission material comprises an electrically conductive substrate (13, 14) and, disposed thereon, electrically conductive particles (11) embedded in, formed in, or coated by a layer of inorganic electrically insulating material (12) (e.g. glass). A first thickness (15) of the insulating material is defined between each particle (11) and the substrate (13, 14) and a second thickness (15) of the insulating material is defined between the particle (11) and the environment in which the material is disposed. The dimension of each particle (11) between the first and second thicknesses (15) is significantly greater than each thickness (15). Upon application of a sufficient electric field, each thickness (15) provides a conducting channel, to afford electron emission from the particles (11). By use of an inorganic insulating material (12), surprisingly good stability and performance have been obtained. The particles (11) can be relatively small, such that the electron emitting material (11, 12) can be applied to the substrate (13, 14) quite cheaply by a variety of methods, including printing. The material can be used in a variety of devices, including display and illuminating devices.

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## FIELD ELECTRON EMISSION MATERIALS AND DEVICES

This invention relates to field electron emission materials, and devices using such materials.

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In classical field electron emission, a high electric field of, for example,  $\approx 3 \times 10^9 \text{ V m}^{-1}$  at the surface of a material reduces the thickness of the surface potential barrier to a point at which electrons can leave the material by quantum mechanical tunnelling. The necessary conditions can be realised using atomically sharp points to concentrate the macroscopic electric field. The field electron emission current can be further increased by using a surface with a low work function. The metrics of field electron emission are described by the well known Fowler-Nordheim equation.

15        There is considerable prior art relating to tip based emitters, which term describes electron emitters and emitting arrays which utilise field electron emission from sharp points (tips). The main objective of workers in the art has been to place an electrode with an aperture (the gate) less than  $1 \mu\text{m}$  away from each single emitting tip, so that the required high fields can be achieved using applied potentials of 100V or less - these emitters are termed gated arrays. The first practical realisation of this was described by C A Spindt, working at Stanford Research Institute in California (*J.Appl.Phys.* 39(7), 3504-3505, 1968). Spindt's arrays used molybdenum emitting tips which were produced, using a self masking technique, by vacuum evaporation of metal into cylindrical depressions in a  $\text{SiO}_2$  layer on a Si substrate.

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In the 1970s, an alternative approach to produce similar structures was the use of directionally solidified eutectic alloys (DSE). DSE alloys have one phase in the form of aligned fibres in a matrix of the other. The matrix can be etched back leaving the fibres protruding. After etching, a gate structure  
5 is produced by sequential vacuum evaporation of insulating and conducting layers. The build up of evaporated material on the tips acts as a mask, leaving an annular gap around a protruding fibre.

A further discussion of the prior art is now made with reference to  
10 Figures 1 and 2 of the accompanying diagrammatic drawings, in which Figure 1 shows basic components of one field electron emission display, and Figure 2 shows the conceptual arrangement of another field electron emission display.

15 An important approach is the creation of gated arrays using silicon micro-engineering. Field electron emission displays utilising this technology are being manufactured at the present time, with interest by many organisations world-wide. Figure 1 shows basic components of such a display in which a field electron emission current is extracted from points  
20 1 by applying a positive potential to gate electrodes 2. The extracted electrons are accelerated by a higher positive potential to a patterned phosphor on conducting strips 3 on a front plate. Pixels are addressed by energising horizontal and vertical stripes in a crossbar arrangement. The device is sealed around the perimeter and evacuated.

25

A major problem with all point based emitting systems is their vulnerability to damage by ion bombardment, ohmic heating at high

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currents and the catastrophic damage produced by electrical breakdown in the device. Making large area devices is both difficult and costly.

5 In about 1985, it was discovered that thin films of diamond could be grown on heated substrates from a hydrogen-methane atmosphere, to provide broad area field emitters.

10 In 1991, it was reported by Wang et al (*Electron. Lett.*, 1991, 27, pp 1459-1461) that field electron emission current could be obtained from broad area diamond films with electric fields as low as  $3 \text{ MV m}^{-1}$ . This performance is believed to be due to a combination of the negative electron affinity of the (111) facets of diamond and the high density of localised, accidental graphite inclusions (*Xu, Latham and Tzeng: Electron. Lett.* 1993, 29, pp 1596-159).

15

Coatings with a high diamond content can now be grown on room temperature substrates using laser ablation and ion beam techniques. However, all such processes utilise expensive capital equipment.

20 S I Diamond in the USA has described a field electron emission display (FED) that uses as the electron source a material that it calls Amorphous Diamond. The diamond coating technology is licensed from the University of Texas. The material is produced by laser ablation of graphite onto a substrate. Figure 2 shows the conceptual arrangement in such a display. A substrate 4 has conducting strips 5 with Amorphous diamond emitting patches 6. A front plate 8 has transparent conducting tracks 7 with an applied phosphor pattern (not shown). Pixels are addressed using a crossbar approach. Negative going waveforms 9 are applied to the

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conductive strips 5 and positive going waveforms are applied to conductive strips 7. The use of positive and negative going waveforms both reduces the peak voltage rating for the semiconductors in the drive electronics and ensures that adjacent pixels are not excited. The device is sealed around the perimeter and evacuated.

Turning now to Composite Field Emitters, current understanding of field electron emission from flat metal surfaces shows that active sites are either metal-insulator-vacuum (MIV) structures formed by embedded dielectric particles or conducting flakes sitting on the surface oxide of the metal. In both cases, the current comes from a hot electron process that accelerates the electrons resulting in quasi-thermionic emission. This is described in the scientific literature (e.g. *Latham, High Voltage Vacuum Insulation, Academic Press 1995*)

In 1988 (*S Bajic and R V Latham J. Appl. Phys. 21 (1988) 200-204*), a material that made practical use of the above mechanism was described. The composite material creates a high density of metal-insulator-metal-insulator-vacuum (MIMIV) emitting sites. The composite had conducting particles dispersed in an epoxy resin. The coating was applied to the surface by standard spin coating techniques.

The emission process is believed to occur as follows. Initially the epoxy resin forms a blocking contact between the particles and the substrate. The voltage of a particle will rise to the potential of the highest equipotential it probes - this has been called the antenna effect. At a certain applied voltage, this will be high enough to create an electro-formed conducting channel between the particle and the substrate. The potential

- 5 -

of the particle then flips rapidly towards that of the cathode. The residual charge above the particle then produces a high electric field which creates a second electro-formed channel and an associated MIV hot electron emission site. After this switch-on process, reversible field emitted currents can be drawn from the site. The current density/electric field performance of this material is equivalent to broad area diamond emitters produced by the much more expensive laser ablation process.

Bajic and Latham worked with resin-carbon composites. Although they considered the use of alternative materials, these were always composites with resin (*supra and Inst Phys Conf Ser No 99; Section 4 - pp-101-104, 1989*). Epoxy resins provided materials that were convenient to work with, particularly in view of their adhesive properties, making it convenient to place and hold particles where desired, in composite or layered structures. However, materials such as those produced by Bajic and Latham have tended to have poor stability, and not to work satisfactorily in sealed-off vacuum devices.

Preferred embodiments of the present invention aim to provide cost effective broad area field emitting materials and devices that utilise such materials. The materials may be used in devices that include: field electron emission display panels; high power pulse devices such as electron MASERS and gyrotrons; crossed-field microwave tubes such as CFAs; linear beam tubes such as klystrons; flash x-ray tubes; triggered spark gaps and related devices; broad area x-ray sources for sterilisation; vacuum gauges; ion thrusters for space vehicles; particle accelerators; ozonisers; and plasma reactors.

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According to one aspect of the present invention, there is provided a field electron emission material comprising an electrically conductive substrate and, disposed thereon, at least one electrically conductive particle embedded in, formed in, or coated by a layer of inorganic electrically insulating material to define a first thickness of the insulating material between the particle and the substrate and a second thickness of the insulating material between the particle and the environment in which the material is disposed, the dimension of said particle between said thicknesses being significantly greater than each said thickness.

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By "significantly greater", it is meant that said dimension of said particle is at least twice each said thickness.

The use of an inorganic electrically insulating material has provided unexpected advantages. Such materials do not naturally suggest themselves as insulators in this context since, as compared to materials such as epoxy resins, they are relatively difficult to work with. However, in preferred embodiments of the invention, emitting materials of surprisingly good stability and performance have been achieved, by using electrically conductive particles in an inorganic electrically insulating material.

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Preferably, said dimension of said particle is at least 10 times greater than each said thickness.

Preferably, said dimension of said particle is at least 100 times greater than each said thickness.

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In a preferred example, said thickness may be of the order of 100 Å and said particle dimension of the order of 100 μm.

5 There may be provided a substantially single layer of said conductive particles each having their longest dimensions in the range 0.1 μm to 400 μm.

Preferably, said inorganic insulating material comprises a material other than diamond.

10

Preferably, said inorganic insulating material comprises a glass, lead based glass, glass ceramic, melted glass or other glassy material, ceramic, oxide ceramic, oxidised surface, nitride, nitrided surface, or boride ceramic.

15 Said inorganic insulating material may comprise substantially undoped diamond.

By substantially undoped diamond is meant diamond that has not undergone intentional doping to facilitate the passage of current.

20

A field electron emission material as above may comprise a plurality of said conductive particles, preferentially aligned with their longest dimension substantially normal to the substrate.

25 A field electron emission material as above may comprise a plurality of conductive particles having a mutual spacing in the range 5 to 15 times their longest dimension.

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A field electron emission material as above may comprise a structure in which said layer of inorganic electrically insulating material comprises an electrically insulating matrix and there are provided a plurality of said electrically conductive particles as an array of conductive fibres substantially supported in said insulating matrix with exposed fibre ends substantially co-planar with the insulating matrix, and the exposed fibre ends and co-planar matrix substantially covered with an electrically insulating sub-layer.

10 Said structure may be bonded by means of an electrically conductive medium to said electrically conductive substrate.

Preferably, the fibres have a length in the range 1  $\mu\text{m}$  to 2 mm and a diameter in the range 0.5  $\mu\text{m}$  to 100  $\mu\text{m}$ .

15

Preferably, the inter-fibre spacing is in the range 5 to 15 times the fibre length.

The fibre array may be formed from a slice of a directionally solidified eutectic material.

20

Preferably, a respective said insulating sub-layer is provided on each of two opposite faces of said structure.

25 Preferably, the thickness of the or each insulating sub-layer is in the range 50  $\text{\AA}$  to 2  $\mu\text{m}$ .

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The or each insulating sub-layer may comprise a glass, glass ceramic, ceramic, oxide ceramic, nitride, boride ceramic or diamond.

5 Preferably, the conductivity of the conducting particle is such that a potential drop caused by the emission current passing through the particle is sufficient to reduce the electric field at the emission point of the particle by an amount that controls the emission current.

10 Preferably, said particle comprises, or at least some of said particles comprise, silicon carbide, tantalum carbide, hafnium carbide, zirconium carbide, the Magneli sub-oxides of titanium, semiconducting silicon, III-V compounds and II-VI compounds.

15 According to another aspect of the present invention, there is provided a method of forming a field electron emission material according to any of the preceding aspects of the invention, comprising the step of disposing the or each said electrically conductive particle on said electrically conductive substrate with the or each said electrically conductive particle embedded in, formed in, or coated by said layer of inorganic electrically  
20 insulating material.

Preferably, said electrically conductive particle(s) and/or inorganic electrically insulating material are applied to said electrically conductive substrate by a printing process.

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A method as above may include the step of sintering or otherwise joining together a mixture of larger and smaller particles, the larger particles comprising a plurality of said conductive particles and the smaller particles

- 10 -

forming said layer of inorganic insulating material. The insulating material may then comprise glass ceramic, ceramic, oxide ceramic, nitride, boride or diamond.

5           A method as above may include the steps of applying sequentially to the substrate an insulating film, conductive particle layer and further insulating film. The insulating material may then comprise a ceramic, oxide ceramic, oxide, nitride, boride or diamond.

10           A method as above may include the steps of applying an insulating coating directly onto each of a plurality of said conductive particles and then fixing the coated particles to the substrate by a glassy material or braze. The insulating material may then comprise glass, glass ceramic, ceramic, oxide ceramic, oxide, nitride, boride or diamond.

15           Said layer of inorganic insulating material may comprise a porous insulator and said method may include the step of filling the pores of the porous insulator with a conductive material to provide a plurality of said conductive particles.

20           A method as above may include the step of forming two outer sub-layers of inorganic insulating material on opposite faces of said porous insulator, so that said porous insulator comprises a middle sub-layer between said two outer sub-layers of inorganic insulating material.

25           The invention extends to a field electron emission material produced by any of the above methods.

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According to a further aspect of the present invention, there is provided a field electron emission device comprising a field electron emission material according to any of the preceding aspects of the invention.

5           A field electron emission device as above may comprise a substrate with an array of emitter patches of said field electron emission material, and a control electrode with an aligned array of apertures, which electrode is supported above the emitter patches by an insulating layer.

10           A field electron emission device as above may comprise a plasma reactor, corona discharge device, silent discharge device or ozoniser.

          A field electron emission device as above may comprise an electron source, electron gun, electron device, x-ray tube, vacuum gauge, gas filled  
15       device or ion thruster.

          The field electron emission material may supply the total current for operation of the device.

20           The field electron emission material may supply a starting, triggering or priming current for the device.

          A field electron emission device as above may comprise a display  
25       device.

          A field electron emission device as above may comprise a lamp.

          Preferably, said lamp is substantially flat.

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The field electron emission material may be applied in patches which are connected in use to an applied cathode voltage via a resistor.

5 Preferably, said resistor is applied as a resistive pad under each emitting patch.

10 A respective said resistive pad may be provided under each emitting patch, such that the area of each such resistive pad is greater than that of the respective emitting patch.

15 Preferably, said emitter material and/or a phosphor is/are coated upon one or more one-dimensional array of conductive tracks which are arranged to be addressed by electronic driving means so as to produce a scanning illuminated line.

20 Such a field electron emission device may include said electronic driving means.

25 The environment may be gaseous, liquid, solid, or a vacuum.

A field electron emission device as above may include a gettering material within the device.

30 Preferably, said gettering material is affixed to the anode.

35 Said gettering material may be affixed to the cathode. Where the field electron emission material is arranged in patches, said gettering material may be disposed within said patches.

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According to another aspect of the present invention, there is provided a field emission display device comprising an anode, a cathode, spacer sites on said anode and cathode, spacers located at at least some of said spacer sites to space said anode from said cathode, and gettering material  
5 located at others of said spacer sites.

In the context of this specification, the term "spacer site" means a site that is suitable for the location of a spacer to space an anode from a cathode, irrespective of whether a spacer is located at that spacer site.  
10

Preferably, said spacer sites are at a regular or periodic mutual spacing.

In a field emission display device as above, said cathode may be optically translucent and so arranged in relation to the anode that electrons  
15 emitted from the cathode impinge upon the anode to cause electro-luminescence at the anode, which electro-luminescence is visible through the optically translucent cathode.

It will be appreciated that the electrical terms "conducting" and  
20 "insulating" can be relative, depending upon the basis of their measurement. Semiconductors have useful conducting properties and, indeed, may be used in the present invention as conducting particles. In the context of this specification, the or each said conductive particle has an electrical conductivity at least  $10^2$  times (and preferably at least  $10^3$  or  $10^4$  times) that  
25 of the inorganic electrically insulating material.

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In the context of this specification, the term "inorganic electrically insulating material" includes inorganic materials with organic impurities and, in particular, includes thin film diamond.

5           For a better understanding of the invention, and to show how embodiments of the same may be carried into effect, reference will now be made, by way of example, to Figures 3 to 13 of the accompanying diagrammatic drawings, in which:

10           Figure 3a shows one example of an improved field electron emission material;

Figure 3b illustrates an alternative material to that of Figure 3a;

15           Figure 4 shows a gated array using an improved field electron emission material;

Figure 5 illustrates steps in an alternative method of producing an improved field electron emission material;

20

Figure 6a illustrates a coated conductive particle;

Figure 6b illustrates one example of an improved field electron emission material using coated conductive particles as shown in Figure 6a;

25

Figure 6c illustrates another example of an improved field electron emission material using coated conductive particles as shown in Figure 6a;



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Figure 7a shows a field electron emission display using an improved field electron emission material;

Figures 7b and 7c are detail views showing modifications of parts of  
5 the display of Figure 7a;

Figure 8a shows a flat lamp using an improved field electron emission material and Figure 8b shows a detail thereof;

10 Figure 9 illustrates a further method of producing an improved field electron emission material;

Figure 10a shows an alternative, high performance embodiment of the invention;  
15

Figure 10b shows a detail of the embodiment of Figure 10a;

Figure 11 shows a variant of the embodiment of Figures 10a and 10b;

20 Figure 12a illustrates a self-buffering effect in a conductive particle;

Figure 12b shows measured voltage-current characteristics for emitters with graphite and silicon carbide patches;

25 Figure 13 shows two pixels in a colour display, utilising a triode system with a control electrode;

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Figure 14 shows a display in which spacers are replaced with gettering material;

Figure 15 shows a display in which getter patches are disposed within emitter patches;

Figure 16 illustrates a getter particle used to make a MIMIV emitter;

Figures 17 a and 17b illustrate respective methods of making a structure with a porous insulating layer;

Figure 18 illustrates a high conversion efficiency field emission lamp with light output through the emitter layer; and

Figure 19 shows a sub-pixel of an electrode system, where the gate to emitter spacing has been reduced.

The illustrated embodiments of the invention provide materials based upon the MIMIV emission process with improved performance and usability, together with devices that use such materials.

Heating effects in electro-formed channels limit the mean current available from MTV and MIMIV emitters. Furthermore, the increased temperatures degrade the material, changing its properties and causing instability or catastrophic failure.

The temperature rise in a channel ( $\Delta T$ ) is described by equations of the form

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$$\Delta T = 2 \beta_2 E_0 I / \pi K \epsilon_r \log(l/a)$$

Where:  $\beta_2$  is the field enhancement factor due to the geometry of the channel;  $E_0$  is the gap field;  $I$  is the current flowing in the channel;  $K$  is the thermal conductivity of the medium;  $\epsilon_r$  is the dielectric constant of the medium;  $a$  is the radius of the channel; and  $l$  is the length of the channel.

Figure 3a shows an improved material with conducting particles 11 in an inorganic matrix such as a glass 12 on a conducting substrate 13. This structure increases the thermal conductivity of the matrix 12 approximately four times, as compared to conventional materials. Of equal importance is the increased thermal stability of the inorganic matrix. These two factors combine to produce a material that can supply a significantly higher current, before channel heating causes instability or failure. An inorganic matrix also eliminates high vapour pressure organic materials, enabling the material to be used in sealed-off vacuum devices. For insulating substrates 13, a conducting layer 14 is applied before coating. The conducting layer 14 may be applied by a variety of means including, but not limited to, vacuum and plasma coating, electro-plating, electroless plating and ink based methods.

20

The standing electric field required to switch on the electro-formed channels is determined by the ratio of particle height 16 and the thickness of the matrix in the region of the conducting channels 15. For a minimum switch on field, the thickness of the matrix 12 at the conducting channels should be significantly less than the particle height. The conducting particles would typically be in, although not restricted to, the range  $0.1 \mu\text{m}$  to  $400 \mu\text{m}$ , preferably with a narrow size distribution.

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Structures of this form may be produced (Figure 3b) by sintering a mixture of large conducting particles 50 mixed with significantly smaller insulating particles 51. Examples of suitable insulating materials are: glass ceramics, oxide ceramics, nitrides, borides although a wide range of other materials may be used. For high current applications, high thermal conductivity materials such as beryllia and aluminium nitride may be used to improve performance.

The structure may also be produced by melting a glass with good flow properties, such as a lead glass, with the particles. Such a structure is shown in Figure 3a. Using glassy materials, the thickness of the channel regions may be controlled by varying the time/temperature profile during firing.

To enable the material to be applied in a controlled manner, it can be formulated as an ink with a no-residue binder similar to materials used for hybrid electronic circuits. Such a binder may be photosensitive to enable patterning by photo-lithography. Using an ink so prepared, the emitter may be applied in patterns using hybrid microcircuit techniques such as screen printing. Alternative application methods may be used including, but not limited to, offset lithography, ink-jet printing, electrostatic coating (optionally with photo-resist), Xerography, brush coating, electrophoresis, plasma or flame spraying and sedimentation. Thus, the field emitting material may be printed onto a suitable substrate, opening up new opportunities for economical fabrication of displays, etc.

One suitable ink can be formulated from a mixture of a spin-on glass material, particles (optionally with a narrow size distribution) a dispersing

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agent and a binder. Such spin-on glass materials are typically based on polysiloxanes and are used extensively in the semiconductor industry. However, spin-on glasses based upon other chemical compounds may be used.

5

Figure 5 shows an alternative method of producing desired structures. A conducting substrate 24, which could be produced by over-coating an insulating substrate, has an insulating film 25 deposited upon it. Such a film may be produced by, but not limited to, vacuum or plasma based coating, spin coating and in situ growth by chemical reaction or anodic processes. Conducting particles 26 are then deposited as a layer on the insulating film 25 by a dry coating technique such as, but not limited to, electrostatic coating, Xerography or brush coating. During this stage, electrostatic or magnetic fields may be used to align the particles to achieve optimum electric field enhancement. An insulating coating 27 is then deposited over the particles by typically a vacuum or plasma based process.

Figure 6a shows a conductive particle 28 pre-coated with an insulating film 29 by methods which include: vacuum or plasma based coating, chemical vapour deposition, anodic processes. A plurality of such coated particles 30 are then fixed to the substrate 31 by a glassy material or braze alloy 32, as shown in Figure 6a. Examples of acceptable materials are lead glasses and reactive braze alloys such as Zr-Cu eutectic.

25 In the alternative material shown in Figure 6b, a plurality of coated particles 30 are fixed directly to the substrate 31. In this case, the insulating film 29 is of a material suitable to be fixed directly to the substrate 31 - eg glass.

- 20 -

Figure 9 shows an alternative approach in which a substrate 70 is first coated with an insulating film 71. A much thicker porous insulating film 72 is then applied. A conducting material 73 is then infiltrated into the pores by chemical reaction, electroplating or another method. Finally, a  
5 second thin insulating film 74 is applied.

In all the above-described embodiments of the invention, there is an optimum density of conducting particles that prevents the nearest-neighbour particles screening the electric field at the tip of a given particle. For  
10 spherical particles, the optimum particle-to-particle spacing is approximately 10 times the particle diameter.

Intentionally engineered structures like those in Figure 3a are a substantial improvement upon relatively small, randomly created graphite  
15 inclusions in thin film diamond. An important feature is that the ratio of particle height 16 to insulator barrier thickness 15 is much greater than in diamond films. As a result, the increased antenna effect significantly reduces the switch-on field.

20 To facilitate even switch-on of emitting sites, symmetrical particles, such as those of a rough hewn cuboid shape are preferred.

Alternatively, precision fibres, such as carbon fibre or fine wire, may be chopped into lengths somewhat longer than their diameter. The  
25 tendency of these fibre segments will be to lie down (especially during spin coating) with the fibre axis parallel to the substrate such that the diameter of the fibre determines the antenna effect.

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Particles of the correct morphology (e.g. glass microspheres) but not composition may be over coated with a suitable material by a wide range of processes including sputtering.

5           A primary purpose of preferred embodiments of the invention is to produce emitting materials with low cost and high manufacturability. However, for less cost-sensitive applications, the very high thermal conductivity that may be achieved means that intentionally engineered structures, using diamond as the insulator, can provide materials that can  
10 deliver the highest mean currents before catastrophic failure of the electro-formed channels.

Figure 4 shows a gated array using one of the improved field electron emission materials. Emitter patches 19 are formed on a substrate 17 on  
15 which a conducting layer 18 is deposited, if required, by a process such as screen printing. A perforated control or gate electrode 21 is insulated from the substrate 17 by a layer 20. Typical dimensions are emitter patch diameter (23) 100  $\mu\text{m}$ ; gate electrode-substrate separation (22) 20  $\mu\text{m}$ . A positive voltage on the gate electrode 21 controls the extraction of electrons  
20 from the emitter patches 19. The electrons 53 are then accelerated into the device 52 by a higher voltage 54. The field electron emission current may be used in a wide range of devices including: field electron emission display panels; high power pulse devices such as electron MASERS and gyrotrons; crossed-field microwave tubes such as CFAs; linear beam tubes such as  
25 klystrons; flash x-ray tubes; triggered spark gaps and related devices; broad area x-ray sources for sterilisation; vacuum gauges; ion thrusters for space vehicles and particle accelerators.

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It is known that an MIV process emits electrons with energies of a few electron volts. The mean free path of such electrons in a solid is surprisingly long. Thus, if the emitter material has a thin (eg less than 1000 Å) conducting layer deposited on the surface, and is biased a few hundred volts positive with respect to the substrate, MIMIV processes will occur. With such a thin conducting layer, the majority of emitted electrons will pass through the conducting layer into the environment. Such a conducting layer may be used as a control electrode to modulate the emitted current in a wide range of devices. Such a conducting layer may be used in many embodiments of the invention.

An alternative high performance embodiment of the invention is shown in Figures 10a and 10b. A regular array of fibres 80 is embedded in an insulating matrix 81. The length of the fibres is typically a few hundred microns. Such structures can be fabricated or may be found naturally in directionally solidified ceramic-metal eutectic systems. The inter-fibre spacing (82) is typically several times the fibre length.

The composite so formed is cut into slices and each face is preferably (although optionally) polished. The two polished faces are then coated with an inorganic insulating film 83 of a controlled thickness - typically around 100 Å. The film 83 may be of, but not limited to, glass, glass ceramic, ceramic, oxide ceramic, nitride, boride ceramic or diamond and may be deposited by vacuum coating, ion beam processing, chemical vapour deposition, laser ablation or other appropriate method.

The sandwich structure so formed is then bonded to a substrate 85 using a conducting layer 84. Such a bond could be formed using an active



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metal brazing alloy. Alternatively, the surface to be bonded may be metallised prior to brazing using a non-reactive alloy.

5 The array can provide all the current for a device or act as a trigger for plasma processes (eg spark gaps) or starting current for sources that use secondary emission multiplication (eg magnetron injection guns).

10 If the material of Figures 10a and 10b is for use in a non-vacuum environment, the insulating material 81 may comprise a relatively low-grade material, such as a cheap resin simply to support the fibres 80, provided that the insulating films 83 are of an inorganic material.

15 In the variant of Figure 11, fibres 90 protrude above the level of the insulating material 81, and are covered by a respective film 91 of inorganic insulating material. Otherwise, the embodiment is generally similar to those described above with reference to Figures 10a and 10b.

20 Figure 7 shows a field emission based upon a diode arrangement using one of the above-described materials - eg the material of Figure 9. A substrate 33 has conducting tracks 34 which carry emitting patches 35 of the material. A front plate 38 has transparent conducting tracks 39 running across the tracks 34. The tracks 39 have phosphor patches or stripes. The two plates are separated by an outer ring 36 and spacers 43. The structure is sealed by a material 37 such as a solder glass. The device is evacuated  
25 either through a pumping tube or by fusing the solder glass in a vacuum furnace.

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Pixels are addressed by voltages 41, 42 applied in a crossbar fashion. The field emitted electrons excite the phosphor patches. A drive system consisting of positive and negative going waveforms both reduces the peak voltage rating for the semiconductors in the drive electronics, and ensures that adjacent pixels are not excited. Further reductions in the voltage swing needed to turn pixels on can be achieved by DC biasing each electrode to a value just below that at which the field electron emission current becomes significant. A pulse waveform is then superimposed on the DC bias to turn each pixel on: voltage excursions are then within the capability of semiconductor devices.

An alternative approach to the diode arrangement is to utilise a triode system with a control electrode. Figure 13, which depicts two pixels in a colour display, shows one embodiment of this approach. For pictorial simplicity only two pixels are shown. However the basic structure shown may be scaled up to produce large displays with many pixels. A cathode substrate 120 has conducting tracks 121 coated onto its surface to address each line in the display. Such tracks may be deposited by vacuum coating techniques coupled with standard lithographic techniques well known to those skilled in the art; by printing using a conducting ink; or many other suitable techniques. Patches 122 of the emitting material described above are disposed, using the methods described previously, onto the surface of the tracks to define sub-pixels in a Red-Green-Blue triad. Dimension "P" 129 is typically in, although not limited to, the range 200  $\mu\text{m}$  (micrometer) to 700  $\mu\text{m}$ . Alternatively, although less desirable, the emitting material may be coated over the whole display area. An insulating layer 123 is formed on top of the conducting tracks 121. The insulating layer 123 is perforated with one or more apertures per pixel 124 to expose the emitting material

- 25 -

surface, such apertures being created by printing or other lithographic technique. Conducting tracks 125 are formed on the surface of the insulator to define a grid electrode for each line in the colour triad. The dimensions of the apertures 124 and the thickness of the insulator 123 are chosen to produce the desired value of transconductance for the triode system so produced. The anode plate 126 of the display is supported on insulating spacers 128. Such spacers may be formed on the surface by printing or may be prefabricated and placed in position. For mechanical stability, said prefabricated spacers may be made in the form of a cross-shaped structure.

10 A gap filling material, such as a glass frit, may be used to fix both the spacer in position at each end and to compensate for any dimensional irregularities. Red, green and blue phosphor patches or stripes 127 are disposed on the inside surface of the anode plate. The phosphors are either coated with a thin conducting film as is usual in cathode ray tubes or, for lower accelerating voltages, the inside of the anode plate has deposited on it a transparent conducting layer such as, but not limited to, indium tin oxide. The interspace between the cathode and anode plates is evacuated and sealed.

20 A DC bias is applied between conducting strips 121 and the conducting film on the anode. The electric field so produced penetrates through the grid apertures 124 and releases electrons from the surface by field emission from the MIMIV field emission process described earlier. The DC voltage is set lower than required for full emission thus enabling a line to be addressed by pulsing one of the tracks 121 negative with respect to the others to a value that gives the current for peak brightness. The grid tracks 125 are biased negative with respect to the emitter material to reduce the current to its minimum level when the tracks 121 are in their negative

25

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pulsed (line addressed) state. During the line period all grid tracks are pulsed positively up to a value that gives the desired current and hence pixel brightness. Clearly other driving schemes may be used.

5           To minimise the cost of the drive electronics, gate voltage swings of a few tens of volts are needed. To meet this specification, the apertures in the gate electrode structures shown in Figure 13 become quite small. With circular apertures, this results in many emitting cells per sub-pixel. An alternative arrangement for such small structures is to elongate the small  
10   emitting cells into slots.

Figure 19 shows one sub-pixel of such an electrode system, where the gate to emitter spacing 180 has been reduced to a few micrometres. The gate 181 and insulator layer 182 have slots 183 in them, exposing the  
15   emitting material.

Although a colour display has been described, it will be understood by those skilled in the art that an arrangement without the three-part pixel may be used to produce a monochrome display.

20

To ensure a long life and stable operating characteristics a high vacuum must be maintained in the device. It has been normal in the art of electron tubes to use getters to adsorb gas desorbed from the walls and other internal structures. One location for gettering materials in field emitting  
25   displays is around the perimeter of the display panel on those sides where there are no electrical feedthroughs. It is well known to those skilled in the art that this location becomes far from ideal as the panel size increases. This is because of the low gas flow conductance between the centre and the edge

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of the panel that results from the long distances and sub-millimetre clearances between the panels. Calculations show that for panels greater than a 250 mm diagonal dimension this conductance drops to a level where the getter system becomes ineffective. US Patent 5,223,766 describes two methods of overcoming this problem. One method involves a cathode plate with an array of holes leading into a back chamber with larger clearances and distributed getters. The other method is to make the gate electrode of a bulk gettering material such as zirconium. Although both methods work in principle there are distinct practical problems with them.

10

In the perforated cathode plate approach, the perforations in the cathode plate must be small enough to fit within the spaces between the pixels. To avoid visible artefacts this limits their diameter to a maximum of 125 micrometers for television and rather less for computer workstations. The cost of drilling millions of  $\sim 100$  micrometers holes in 1 mm to 2 mm thick glass, the obvious material for the cathode plate, is likely to be prohibitive. Furthermore, the resulting component will be extremely fragile: a problem that will increase with increasing panel dimensions.

20

In order to be effective at room temperature, bulk getters must have a very high surface area. This is usually achieved by forming a sintered particulate layer. The gate electrode in a field emitting display sits in a strong accelerating DC field. It is clear from the field emitter systems described herein that such particulate getter layers are likely to provide a significant number of field emitting sites. Such sites will emit electrons continuously exciting one or more of the phosphor patches in the vicinity to produce a visible defect in the display.

25

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Turning now to the display shown in Figure 13 three methods are described by which a distributed getter system may be incorporated into the structure. Whilst such methods are described in the context of this display using the emitter systems described herein, it will be understood that the techniques may be used with displays using other emitter systems.

A suitable location for a particulate getter material such that it does not cause spurious emission is the anode plate. At the anode the standing electric field totally suppresses electron emission. In a field emission display the cathode and anode plates are subjected to large forces by the external atmospheric pressure. To prevent distortion and fracture, spacers are disposed between the plates. Said spacers are incorporated into the pixel structure. In order to minimise visible artefacts, obscuring lines are printed onto the anode plate to hide the spacer contact areas. Whilst it is usual to repeat the spacers with the periodicity of the pixels, such an arrangement results in significant mechanical over-design. It is thus possible to reduce the frequency of spacers and to locate gettering material on the anode plate behind the obscuring lines. Figure 14 shows one embodiment with a cathode plate 130 and anode plate 131 supported on spacers 133. The spacer contact areas on the anode plate are masked by obscuring lines 134. In this embodiment spacers are removed from two potential locations and replaced with gettering material 135. Suitable gettering materials are finely divided Group IVa metals such as Zirconium and proprietary gettering alloys such as those produced by SAES Getters of Milan. Such gettering material may be in the form of particles bonded to the anode plate by brazing or glass fritts. Equally it may be directly deposited as a porous layer by a wide range of methods including thermal spraying and vapour coating in an inert scattering gas. Clearly other methods may be devised. Said getters are

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activated during fritt sealing of the structure, passivated upon exposure to air and then reactivated during the bakeout phase of vacuum processing.

5 An alternative method is to locate gettering material within the emitter areas such that any field emitted electrons are modulated along with intentionally emitted electrons and such that spurious electrons augment those from the emitter patches. Figure 15 shows one embodiment of this in which getter patches 170 are disposed within emitter patches 171 such that spurious electrons only excite the phosphor patches 172 when addressed  
10 by the drive electronics.

Figure 16 shows another approach in which a getter particle, or cluster of particles, is used to make a MIMIV emitter as described above. The emission mechanism does not require the particle to be entirely coated  
15 in insulator since the critical areas are the contact point with the substrate and the emitting area towards the top of the particle. In this embodiment a particle 140 is fixed to a substrate 141 by an insulating material 142. The upper portions of the particle are coated with an insulating layer 143. The compositions of the insulating materials 142 and 143 are as described herein.  
20 This arrangement leaves an area of exposed gettering material 144.

Alternatively the insulating layer may coat the entire particle but be substantially porous. Figure 17 shows two method of making such structures. Figure 17a shows particles 151 bonded to a substrate 150 by an  
25 insulating material 152. The upper portions of the particles are coated with an insulator 153 by means of a roller 154. Material is dispensed onto the roller by a system 155. An alternative method, shown in Figure 17b, is to take a substrate with particles bonded as described above and to vacuum

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evaporate an insulating material 161 from a point or line source 162 such that the evaporated material impinges on the surface at an oblique angle. Shadowing ensures that only the top and one side of the particles are coated.

To ensure a uniform insulator thickness the substrate is traversed past the  
5 source.

A problem with all field electron emission displays is in achieving uniform electrical characteristics from pixel to pixel. One approach is to use electronics that drive the pixels in a constant current mode. An alternative  
10 approach that achieves substantially the same objective is to insert a resistor of appropriate value between the emitter and a constant voltage drive circuit. This may be external to the device. However, in this arrangement, the time constant of the resistor and the capacitance of the conducting track array places a limit on the rate that pixels can be addressed. Forming the resistor  
15 in situ between the emitter patch and the conducting track enables low impedance electronics to be used to rapidly charge the track capacitance, giving a much shorter rise time. Such an in situ resistive pad 44 is shown in Figure 7b. The resistive pad may be screen printed onto the conducting track 34, although other coating methods may be used. In some  
20 embodiments, the voltage drop across the resistive pad 44 may be sufficient to cause voltage breakdown across its surface 45. To prevent breakdown, an oversize resistive pad 46 may be used to increase the tracking distance, as illustrated in Figure 7c.

25 The mechanism of operation of the MIMIV emitters previously described offers an alternative method of buffering the emission to resistive pads. In the publication *S Bajic and R V Latham (1988) J. Appl. Phys. 21 200-204* it is proposed that, after "switch-on", current flows from the substrate



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via an electroformed channel, into the particle and is then emitted into the vacuum from a further conducting channel at another point on the particle. This mechanism is shown diagrammatically in figure 12a. It can be seen from this diagram that the emitted current 113 must flow through the particle 110 to be emitted into the vacuum. Between the two conducting channels 112 is the internal resistance of the particle 114. Current flowing from the substrate 109 causes a potential drop across the particle that depends on its resistivity. This potential drop reduces the field at the top of the particle which, in turn, limits the rate of rise of current with electric field. Thus, a self-buffering effect is achieved.

Figure 12b shows measured voltage-current characteristics for emitters with graphite 115 and silicon carbide 116 particles. Over a large range the emitter using silicon carbide particles displays a linear, rather than Fowler-Nordheim-like, voltage-current characteristic. The voltage-emission current characteristic is determined by the resistance of the particle rather than the properties of the conducting channels. Process control of particle size and resistivity is far easier than the adventitiously electro-formed channels. An important benefit of this is greater uniformity and substantially reduced temporal fluctuations of emission compared to emitters with graphite particles.

Modelling shows that the potential drop across the particle at the maximum current shown is in excess of 100 volt. The two examples shown are extremes with resistivities differing by at least 1000:1. By choosing particles with intermediate resistivities, a trade-off can be made between the reduced control voltage swing of the Fowler-Nordheim-like characteristic

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and the stability of the heavily buffered linear characteristic. An optimum choice can be made for each application.

Figure 8a shows a flat lamp using one of the above-described materials. Such a lamp may be used to provide backlighting for liquid crystal displays, although this does not preclude other uses such as room lighting.

The lamp comprises a back plate 60 which may be made of a metal that is expansion matched to a light transmitting front plate 66. If the back plate is an insulator, then a conducting layer 61 is applied. The emitting material 62 is applied in patches. To force the system towards equal field emitted current per emitting patch, and hence produce a uniform light source, each patch is electrically connected to the back plate via a resistor. Such a resistor can be readily formed by an electrically resistive pad 69, as shown in Figure 8b. As in Figure 7c, the resistive pad may have a larger area than the emitting patch, to inhibit voltage breakdown across its thickness. A more cost-effective alternative to resistive patches is to use the self-buffering materials described above. The front plate 66 has a transparent conducting layer 67 and is coated with a suitable phosphor 68. The two plates are separated by an outer ring 63 and spacers 65. The structure is sealed by a material 64 such as a solder glass. The device is evacuated either through a pumping tube or by fusing the solder glass in a vacuum furnace. A DC voltage of a few kilovolts is applied between the back plate 60 or the conducting layer 61 and the transparent conducting coating 67. Field emitted electrons bombard the phosphor 68 and produce light. The intensity of the lamp may be adjusted by varying the applied voltage.

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For some applications, the lamp may be constructed with addressable phosphor stripes and associated electronics to provide a scanning line in a way that is analogous to a flying spot scanner. Such a device may be incorporated into a hybrid display system.

5

Although field emission cathodoluminescent lamps as described above offer many advantages over those using mercury vapour (such as cool operation and instant start), they are intrinsically less efficient. One reason for this is the limited penetration of the incident electrons into the phosphor grains compared with that for ultraviolet light from a mercury discharge. As a result, with a rear electron excited phosphor, much of the light produced is scattered and attenuated in its passage through the particles. If light output can be taken from the phosphor on the same side onto which the electron beam impinges, the luminous efficiency may be approximately doubled. Figure 18 shows an arrangement that enables this to be achieved.

In Figure 18 a glass plate 170 has an optically transparent electrically conducting coating 171 (for example, tin oxide) onto which is formed a layer of MIMIV emitter 172 as described herein. This emitter is formulated to be substantially optically translucent and, being comprised of randomly spaced particles, does not suffer from the Moiré patterning that the interference between a regular tip array and the pixel array of an LCD would produce. Such a layer may be formed with, although not limited to, polysiloxane spin-on glass as the insulating component. The coated cathode plate described above is supported above an anode plate by spacers 179 and the structure sealed and evacuated in the same manner as the lamp shown in Figure 8a. The anode plate 177 which may be of glass, ceramic, metal or other suitable material has disposed upon it a layer of a electroluminescent

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phosphor 175 with an optional reflective layer 176, such as aluminium, between the phosphor and the anode plate. A voltage 180 in the kilovolt range is applied between the conducting layer 171 and the anode plate 177 (or in the case of insulating materials a conducting coating thereon). Field  
5 emitted electrons 173 caused by said applied voltage are accelerated to the phosphor 175. The resulting light output passes through the translucent emitter 172 and transparent conducting layer 171. An optional Lambertian or non-Lambertian diffuser 178 may be disposed in the optical path.

10        Embodiments of the invention may employ thin-film diamond with graphite inclusions that are optimized to meet the requirements of the invention - for example, by aligning such inclusions, making them of sufficient size and density, etc. In the manufacture of thin-film diamond, the trend in the art has been emphatically to minimize graphite inclusions,  
15 whereas, in embodiments of the invention, such inclusions are deliberately included and carefully engineered.

      An important feature of preferred embodiments of the invention is the ability to print an emitting pattern, thus enabling complex multi-emitter  
20 patterns, such as those required for displays, to be created at modest cost. Furthermore, the ability to print enables low-cost substrate materials, such as glass to be used; whereas micro-engineered structures are typically built on high-cost single crystal substrates. In the context of this specification, printing means a process that places or forms an emitting material in a  
25 defined pattern. Examples of suitable processes are: screen printing, Xerography, photolithography, electrostatic deposition, spraying or offset lithography.

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Devices that embody the invention may be made in all sizes, large and small. This applies especially to displays, which may range from a single pixel device to a multi-pixel device, from miniature to macro-size displays.

5           The reader's attention is directed to all papers and documents which are filed concurrently with or previous to this specification in connection with this application and which are open to public inspection with this specification, and the contents of all such papers and documents are incorporated herein by reference.

10

All of the features disclosed in this specification (including any accompanying claims, abstract and drawings), and/or all of the steps of any method or process so disclosed, may be combined in any combination, except combinations where at least some of such features and/or steps are  
15           mutually exclusive.

15

Each feature disclosed in this specification (including any accompanying claims, abstract and drawings), may be replaced by alternative features serving the same, equivalent or similar purpose, unless expressly  
20           stated otherwise. Thus, unless expressly stated otherwise, each feature disclosed is one example only of a generic series of equivalent or similar features.

20

The invention is not restricted to the details of the foregoing  
25           embodiment(s). The invention extends to any novel one, or any novel combination, of the features disclosed in this specification (including any accompanying claims, abstract and drawings), or to any novel one, or any novel combination, of the steps of any method or process so disclosed.

## CLAIMS

1. A field electron emission material comprising an electrically conductive substrate and, disposed thereon, at least one electrically  
5 conductive particle embedded in, formed in, or coated by a layer of inorganic electrically insulating material to define a first thickness of the insulating material between the particle and the substrate and a second thickness of the insulating material between the particle and the environment in which the material is disposed, the dimension of said particle  
10 between said thicknesses being significantly greater than each said thickness.
2. A field electron emission material according to claim 1, wherein said dimension of said particle is at least 10 times greater than each said thickness.
- 15 3. A field electron emission material according to claim 2, wherein said dimension of said particle is at least 100 times greater than each said thickness.
- 20 4. A field electron emission material according to claim 1, 2 or 3, wherein there is provided a substantially single layer of said conductive particles each having their longest dimensions in the range 0.1  $\mu\text{m}$  to 400  $\mu\text{m}$ .
- 25 5. A field electron emission material according to claim 1, 2, 3 or 4, wherein said inorganic insulating material comprises a material other than diamond.

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6. A field electron emission material according to claim 5, wherein said inorganic insulating material comprises a glass, lead based glass, glass ceramic, melted glass or other glassy material, ceramic, oxide ceramic, oxidised surface, nitride, nitrided surface, or boride ceramic.

5

7. A field electron emission material according to claim 1, 2, 3 or 4, wherein said inorganic insulating material comprises substantially undoped diamond.

10 8. A field electron emission material according to any of the preceding claims, comprising a plurality of said conductive particles, preferentially aligned with their longest dimension substantially normal to the substrate.

9. A field electron emission material according to any of the preceding  
15 claims, comprising a plurality of conductive particles having a mutual spacing in the range 5 to 15 times their longest dimension.

10. A field electron emission material according to claim 1, comprising a structure in which said layer of inorganic electrically insulating material  
20 comprises an electrically insulating matrix and there are provided a plurality of said electrically conductive particles as an array of conductive fibres substantially supported in said insulating matrix with exposed fibre ends substantially co-planar with the insulating matrix, and the exposed fibre ends and co-planar matrix substantially covered with an electrically insulating sub-  
25 layer.

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11. A field electron emission material according to claim 10, wherein said structure is bonded by means of an electrically conductive medium to said electrically conductive substrate.
- 5 12. A field electron emission material according to claim 10 or 11, wherein the fibres have a length in the range 1  $\mu\text{m}$  to 2 mm and a diameter in the range 0.5  $\mu\text{m}$  to 100  $\mu\text{m}$ .
- 10 13. A field electron emission material according to claim 10, 11 or 12, wherein the inter-fibre spacing is in the range 5 to 15 times the fibre length.
14. A field electron emission material according to any of claims 10 to 13, wherein the fibre array is formed from a slice of a directionally solidified eutectic material.
- 15 15. A field electron emission material according to any of claims 10 to 14, wherein a respective said insulating sub-layer is provided on each of two opposite faces of said structure.
- 20 16. A field electron emission material according to any of claims 10 to 15, wherein the thickness of the or each insulating sub-layer is in the range 50 Å to 2  $\mu\text{m}$ .
- 25 17. A field electron emission material according to any of claims 10 to 16, wherein the or each insulating sub-layer comprises a glass, glass ceramic, ceramic, oxide ceramic, nitride, boride ceramic or diamond.



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18. A field electron emission material according to any of the preceding claims, wherein the conductivity of the conducting particle is such that a potential drop caused by the emission current passing through the particle is sufficient to reduce the electric field at the emission point of the particle  
5 by an amount that controls the emission current.

19. A field electron emission material according to any of the preceding claims, wherein said particle comprises, or at least some of said particles comprise, silicon carbide, tantalum carbide, hafnium carbide, zirconium  
10 carbide, the Magneli sub-oxides of titanium, semiconducting silicon, III-V compounds and II-VI compounds.

20. A field electron emission material substantially as hereinbefore described with reference to any of Figures 3 to 19 of the accompanying  
15 drawings.

21. A method of forming a field electron emission material according to any of the preceding claims, comprising the step of disposing the or each said electrically conductive particle on said electrically conductive substrate  
20 with the or each said electrically conductive particle embedded in, formed in, or coated by said layer of inorganic electrically insulating material.

22. A method according to claim 21, wherein said electrically conductive particle(s) and/or inorganic electrically insulating material are applied to said  
25 electrically conductive substrate by a printing process.

23. A method according to claim 21, including the step of sintering or otherwise joining together a mixture of larger and smaller particles, the

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larger particles comprising a plurality of said conductive particles and the smaller particles forming said layer of inorganic insulating material.

24. A method according to claim 23, wherein the insulating material  
5 comprises glass ceramic, ceramic, oxide ceramic, nitride, boride or diamond.

25. A method according to claim 21 or 22, including the steps of applying sequentially to the substrate an insulating film, conductive particle layer and further insulating film.  
10

26. A method according to claim 25, wherein the insulating material comprises a ceramic, oxide ceramic, oxide, nitride, boride or diamond.

27. A method according to claim 21 or 22, including the steps of applying  
15 an insulating coating directly onto each of a plurality of said conductive particles and then fixing the coated particles to the substrate by a glassy material or braze.

28. A method according to claim 27, wherein the insulating material  
20 comprises glass, glass ceramic, ceramic, oxide ceramic, oxide, nitride, boride or diamond.

29. A method according to claim 21, wherein said layer of inorganic insulating material comprises a porous insulator and said method includes  
25 the step of filling the pores of the porous insulator with a conductive material to provide a plurality of said conductive particles.

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30. A method according to claim 29, including the step of forming two outer sub-layers of inorganic insulating material on opposite faces of said porous insulator, so that said porous insulator comprises a middle sub-layer between said two outer sub-layers of inorganic insulating material.

5

31. A method of forming a field electron emission material, substantially as hereinbefore described with reference to any of Figures 3 to 19 of the accompanying drawings.

10 32. A field electron emission material produced by a method according to any of claims 19 to 30.

33. A field electron emission device comprising a field electron emission material according to claim 32 or any of claims 1 to 20.

15

34. A field electron emission device according to claim 33, comprising a substrate with an array of emitter patches of said field electron emission material, and a control electrode with an aligned array of apertures, which electrode is supported above the emitter patches by an insulating layer.

20

35. A field electron emission device according to claim 32 or 33, comprising a plasma reactor, corona discharge device, electroluminescent device, silent discharge device or ozoniser.

25 36. A field electron emission device according to claim 33, 34 or 35, comprising an electron source, electron gun, electron device, x-ray tube, vacuum gauge, gas filled device or ion thruster.

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37. A field electron emission device according to claim 33, 34 or 35, wherein the field electron emission material supplies the total current for operation of the device.
- 5 38. A field electron emission device according to claim 33, 34 or 35, wherein the field electron emission material supplies a starting, triggering or priming current for the device.
- 10 39. A field electron emission device according to any of claims 33 to 38, comprising a display device.
40. A field electron emission device according to claim 33 or 34, comprising a lamp.
- 15 41. A field electron emission device according to claim 40, wherein said lamp is substantially flat.
- 20 42. A field electron emission device according to claim 39, 40 or 41, wherein the field electron emission material is applied in patches which are connected in use to an applied cathode voltage via a resistor.
43. A field electron emission device according to claim 42, wherein said resistor is applied as a resistive pad under each emitting patch.
- 25 44. A field electron emission device according to claim 43, wherein a respective said resistive pad is provided under each emitting patch, and the area of each such resistive pad is greater than that of the respective emitting patch.

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45. A field electron emission device according to any of claims 40 to 44, wherein said emitter material and/or a phosphor is/are coated upon one or more one-dimensional array of conductive tracks which are arranged to be addressed by electronic driving means so as to produce a scanning  
5 illuminated line.

46. A field electron emission device according to 45, including said electronic driving means.

10 47. A field electron emission device according to any of claims 35 to 46, wherein said environment of said material is a vacuum.

48. A field electron emission device according to claim 47, including a gettering material within the device.  
15

49. A field electron emission device according to claim 48, wherein said gettering material is affixed to the anode.

50. A field electron emission device according to claim 48, wherein said  
20 gettering material is affixed to the cathode.

51. A field electron emission device according to claim 50, wherein the field electron emission material is arranged in patches and said gettering material is disposed within said patches.  
25

52. A field emission display device comprising an anode, a cathode, spacer sites on said anode and cathode, spacers located at at least some of said

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spacer sites to space said anode from said cathode, and gettering material located at others of said spacer sites.

53. A field emission display device according to claim 52, wherein said  
5 spacer sites are at a regular or periodic mutual spacing.

54. A field emission display device according to claim 39 or to any of  
claims 40 to 53 as appendant thereto, wherein said cathode is optically  
translucent and so arranged in relation to the anode that electrons emitted  
10 from the cathode impinge upon the anode to cause electro-luminescence at  
the anode, which electro-luminescence is visible through the optically  
translucent cathode.

55. A field electron emission device substantially as hereinbefore described  
15 with reference to any of Figures 3 to 19 of the accompanying drawings.

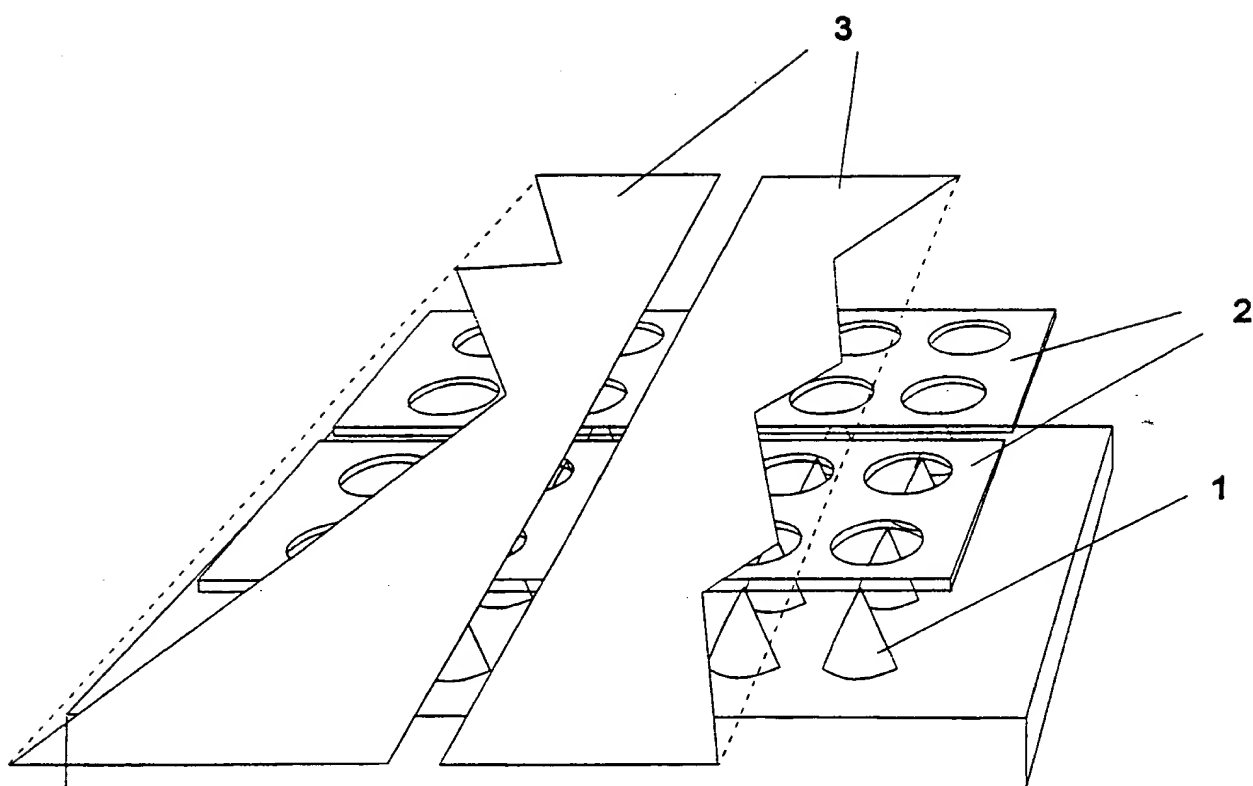


Figure 1

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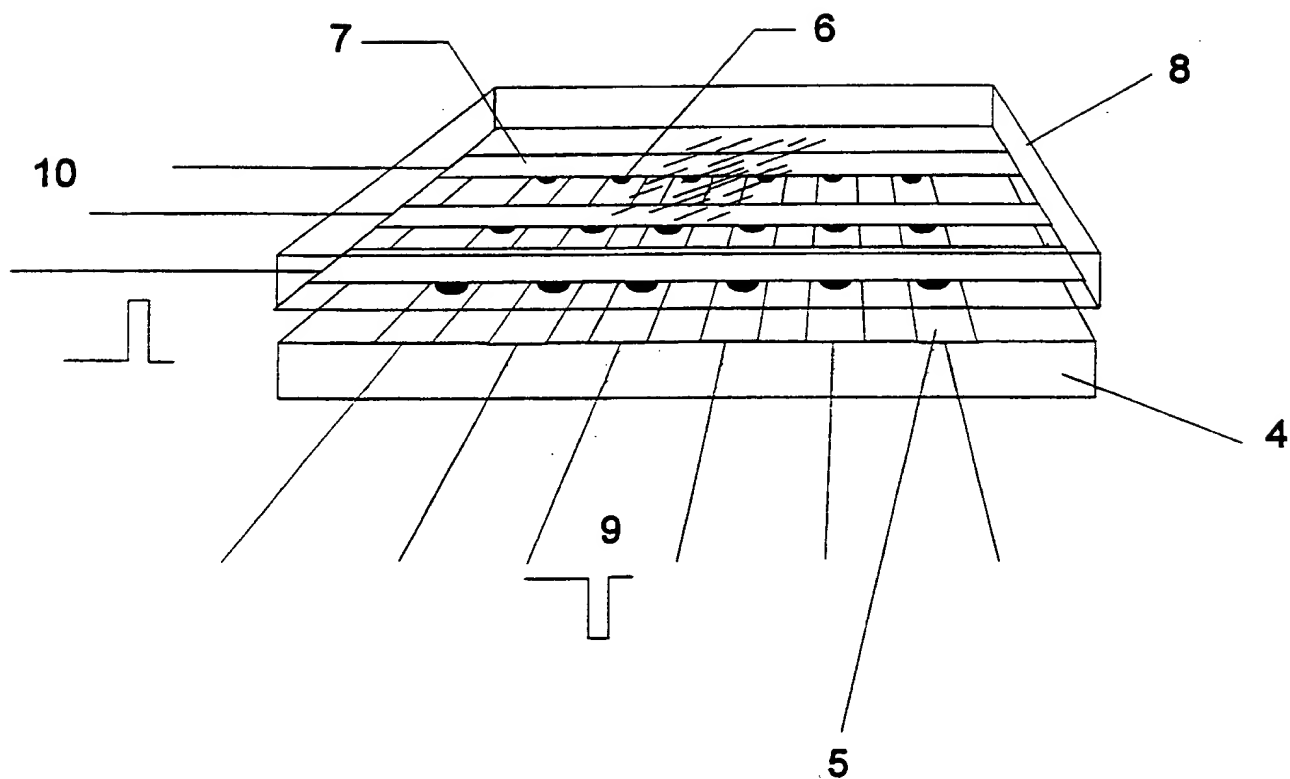
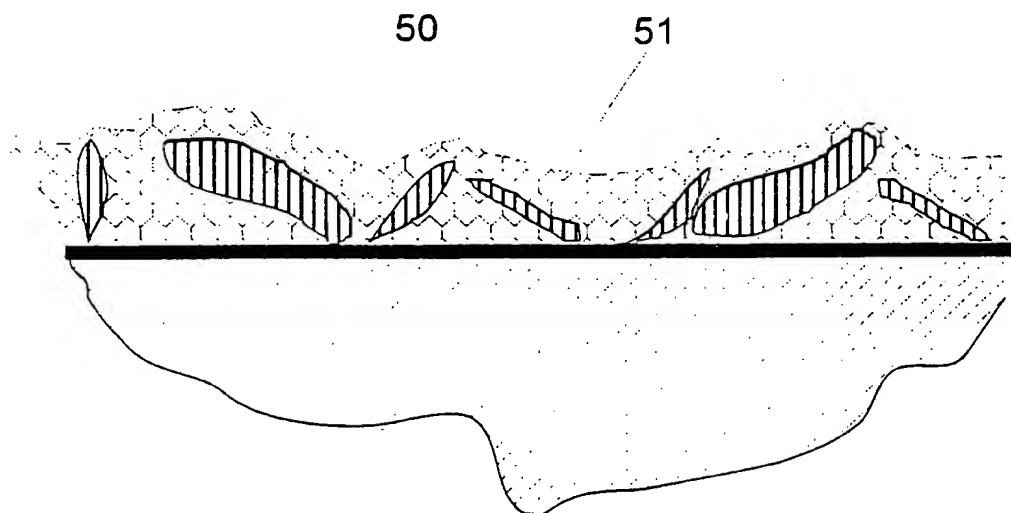
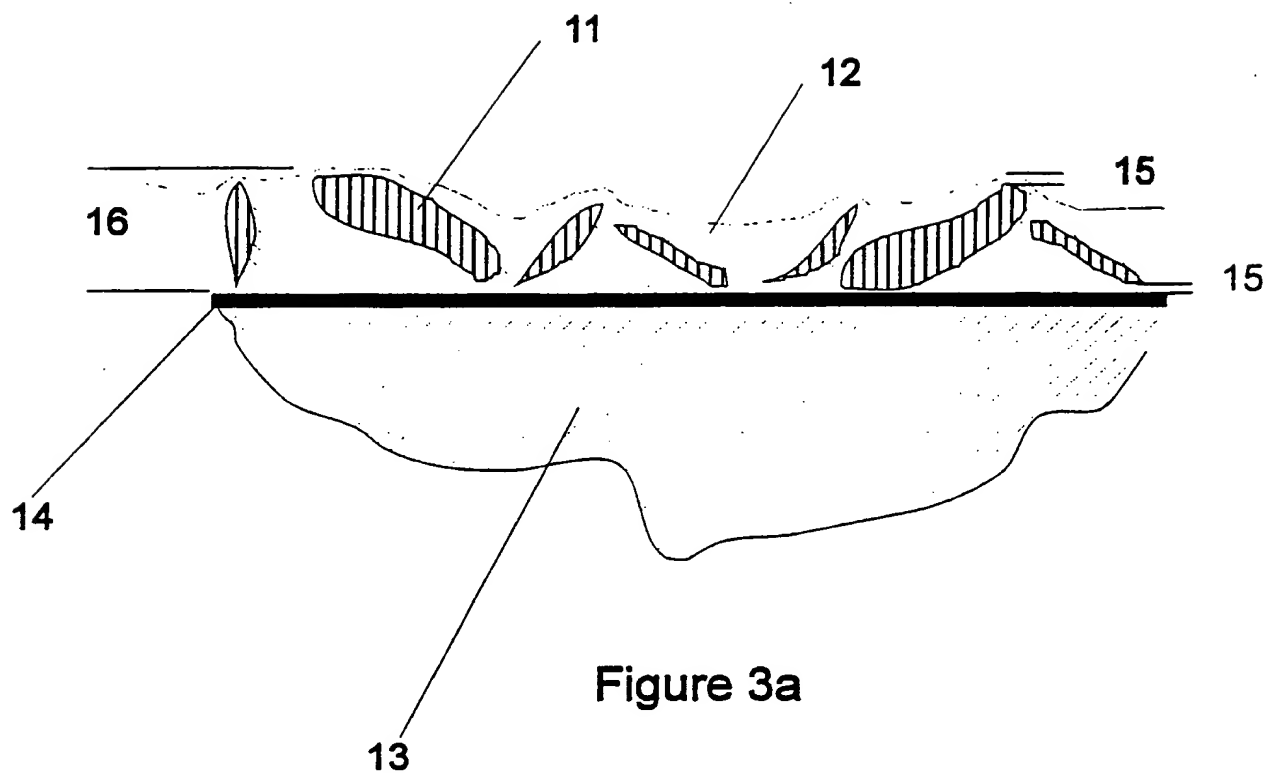


Figure 2



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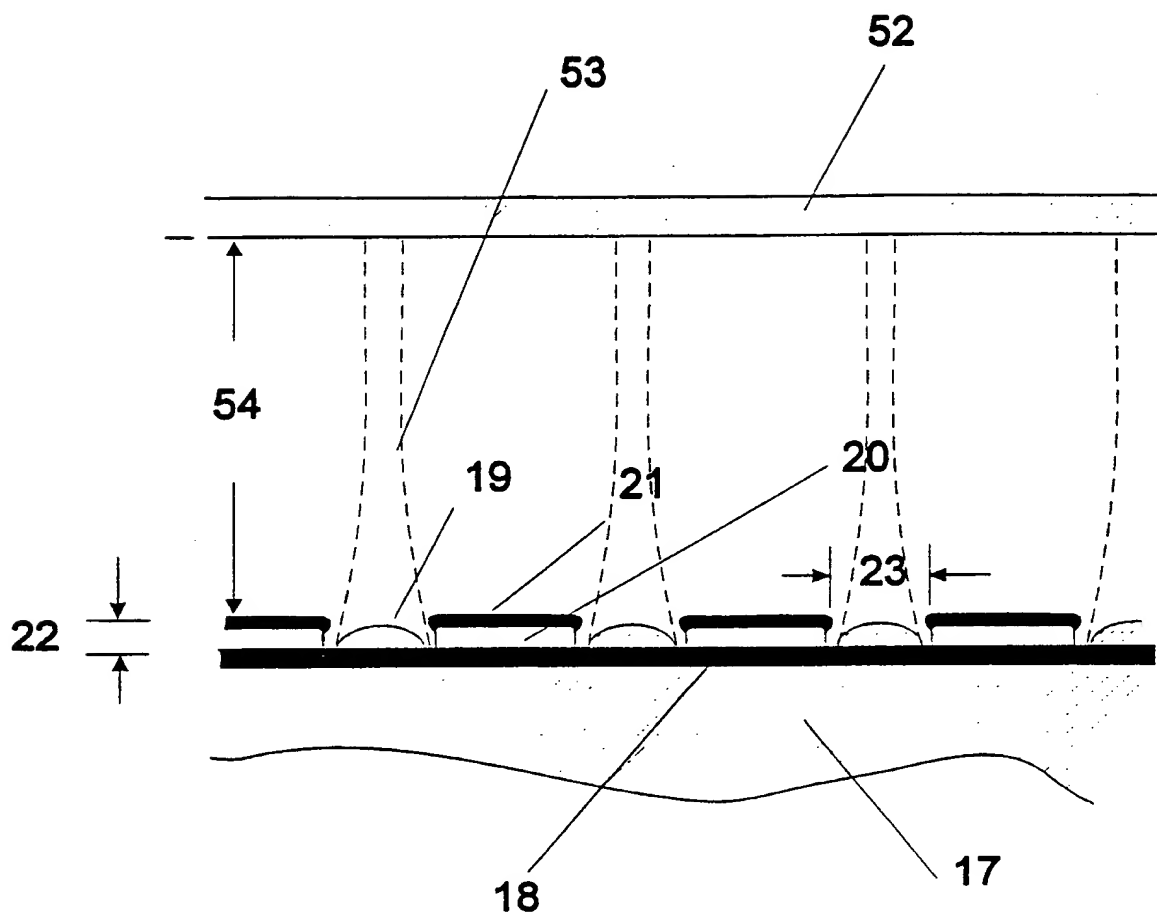


Figure 4

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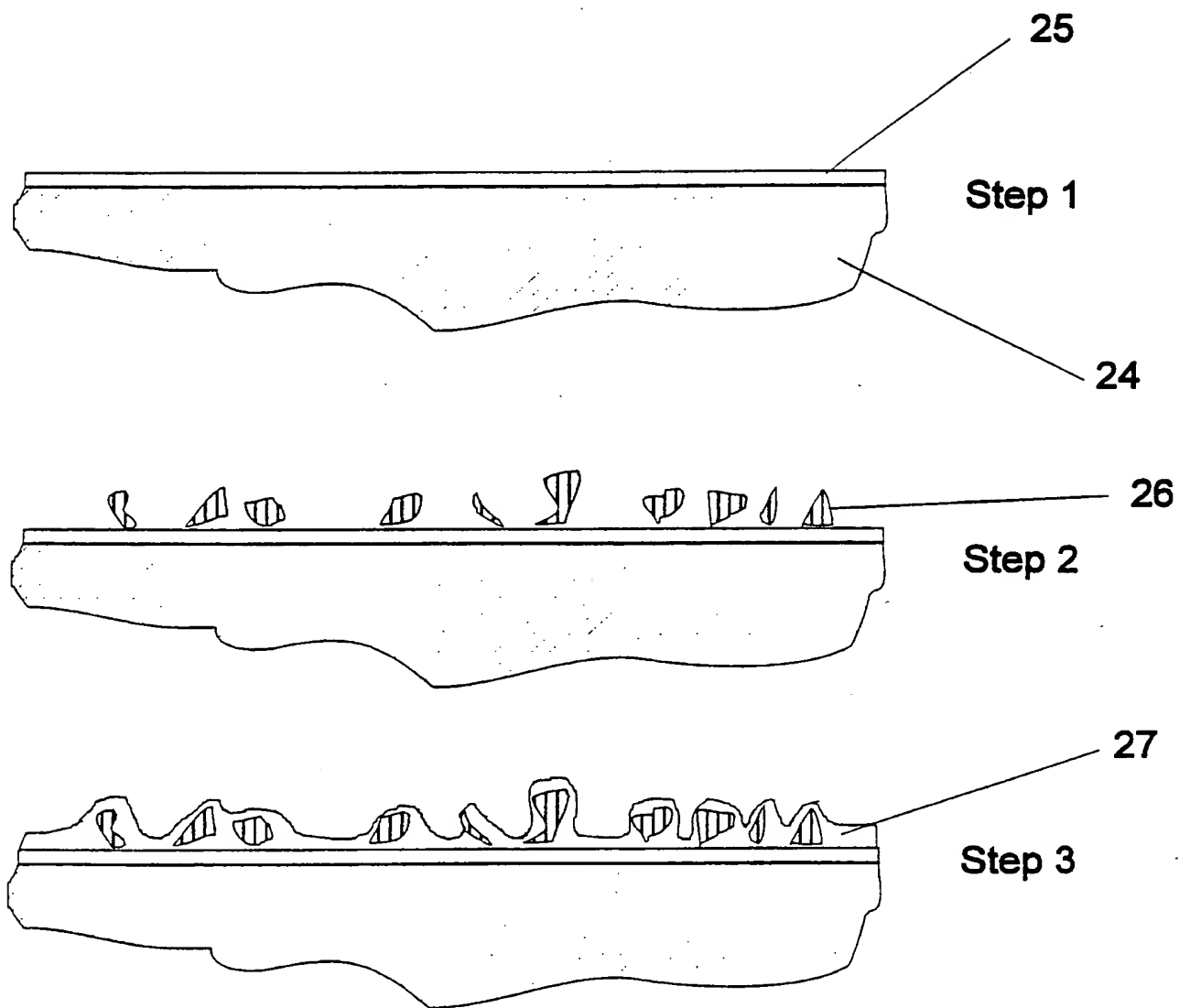


Figure 5

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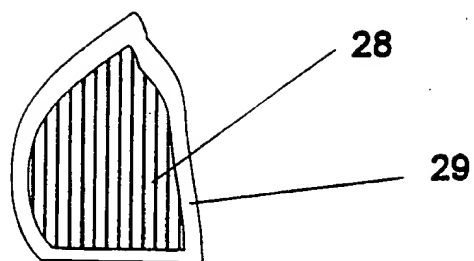


Figure 6a

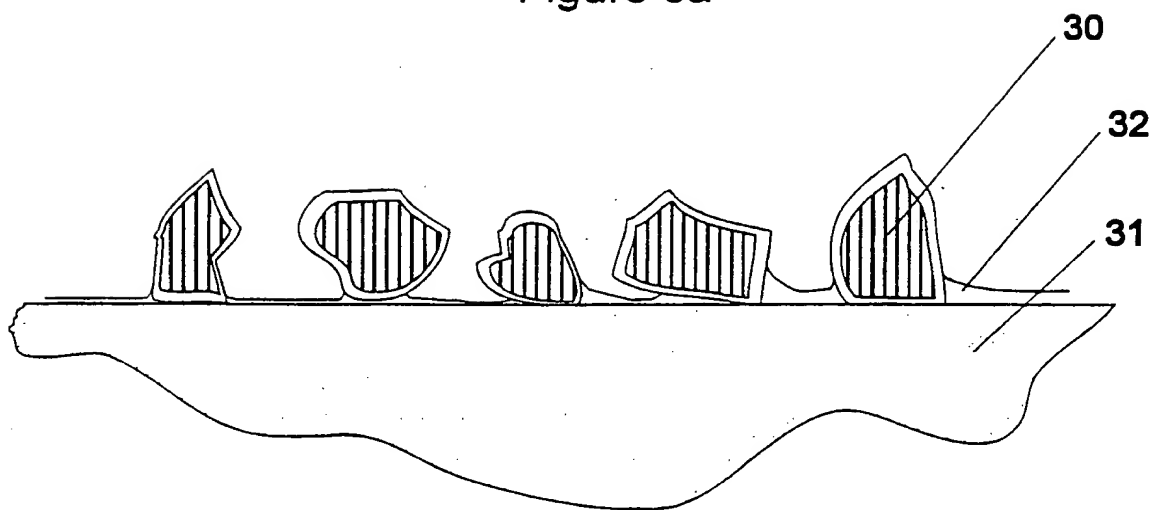


Figure 6b

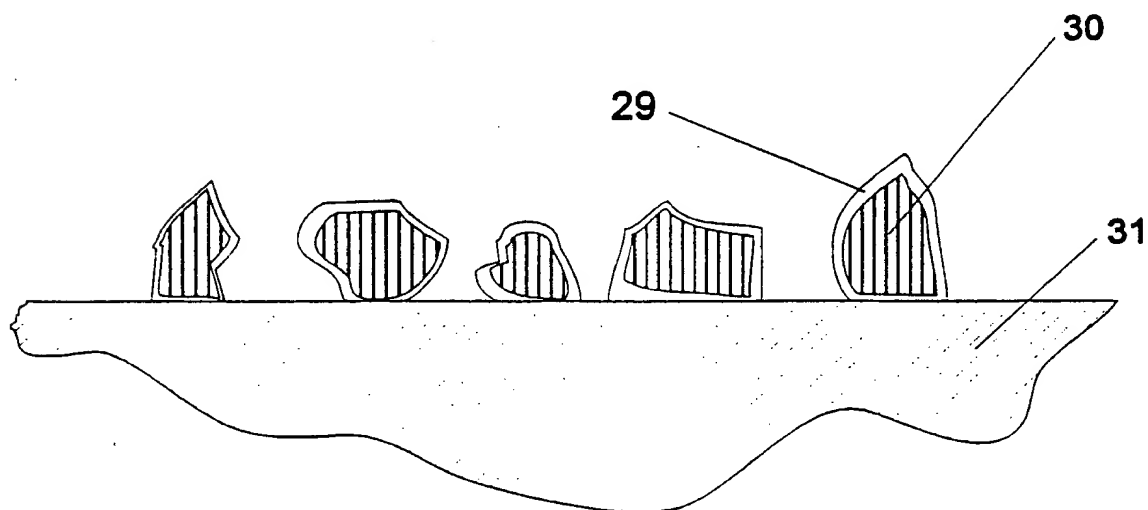


Figure 6c

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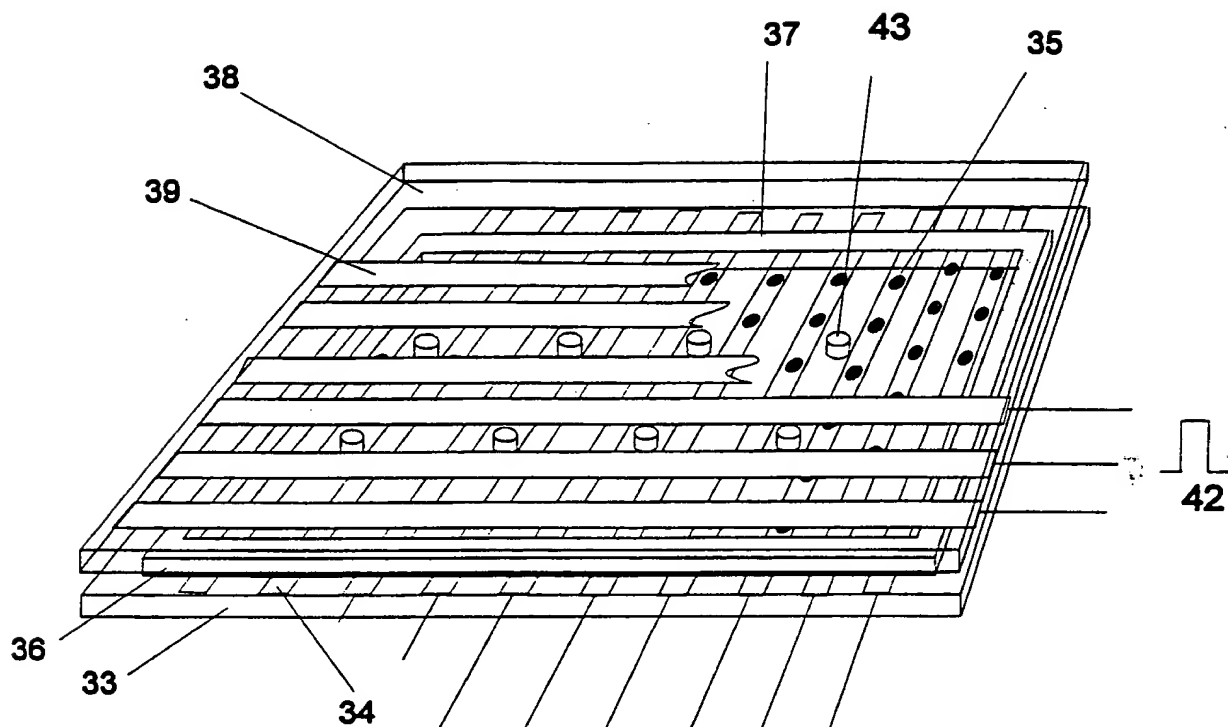


Figure 7a

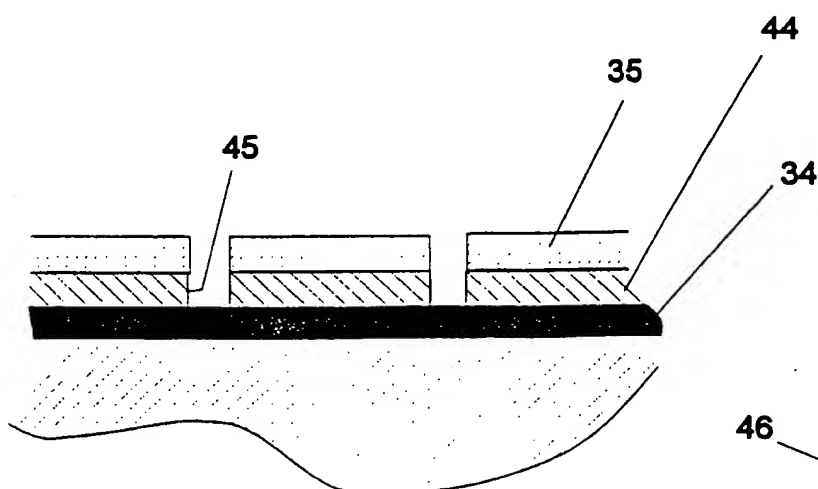


Figure 7b

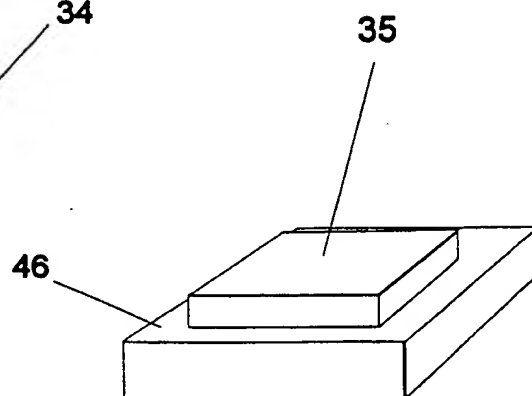


Figure 7c

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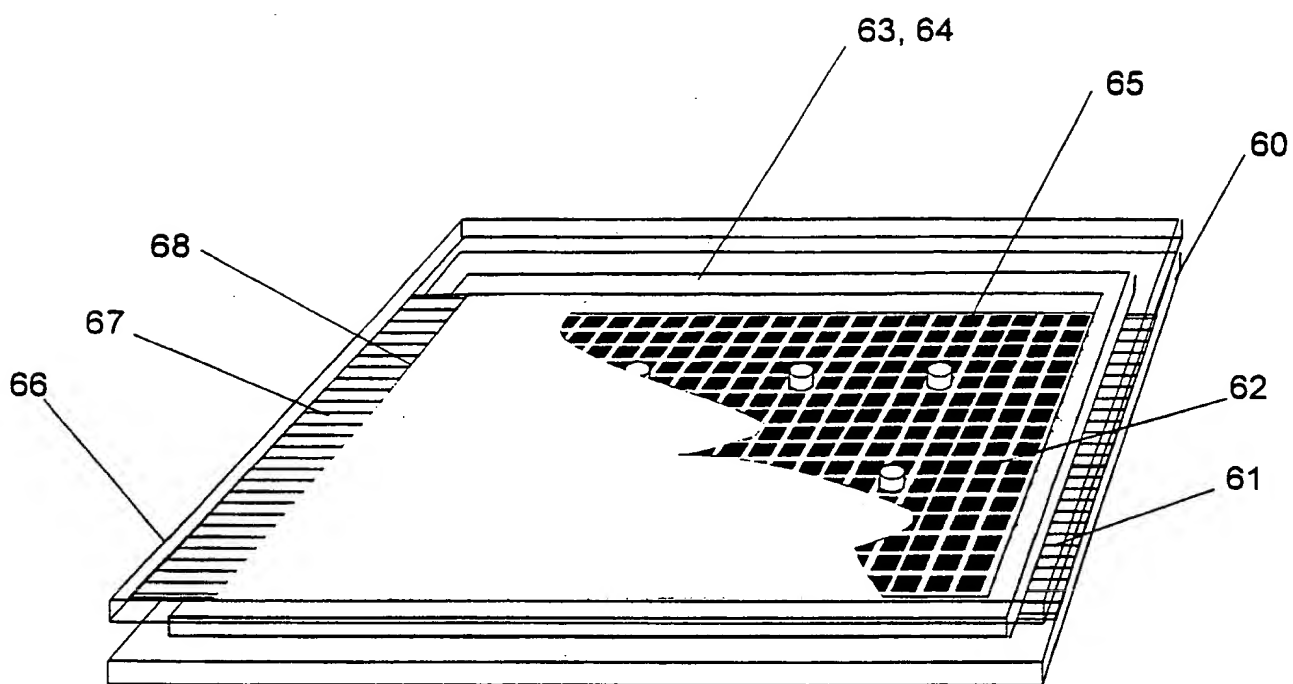


Figure 8a

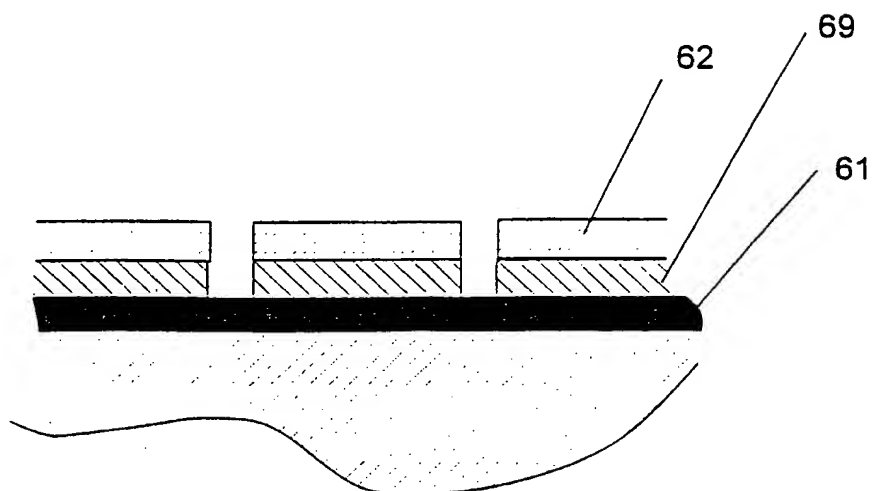


Figure 8b

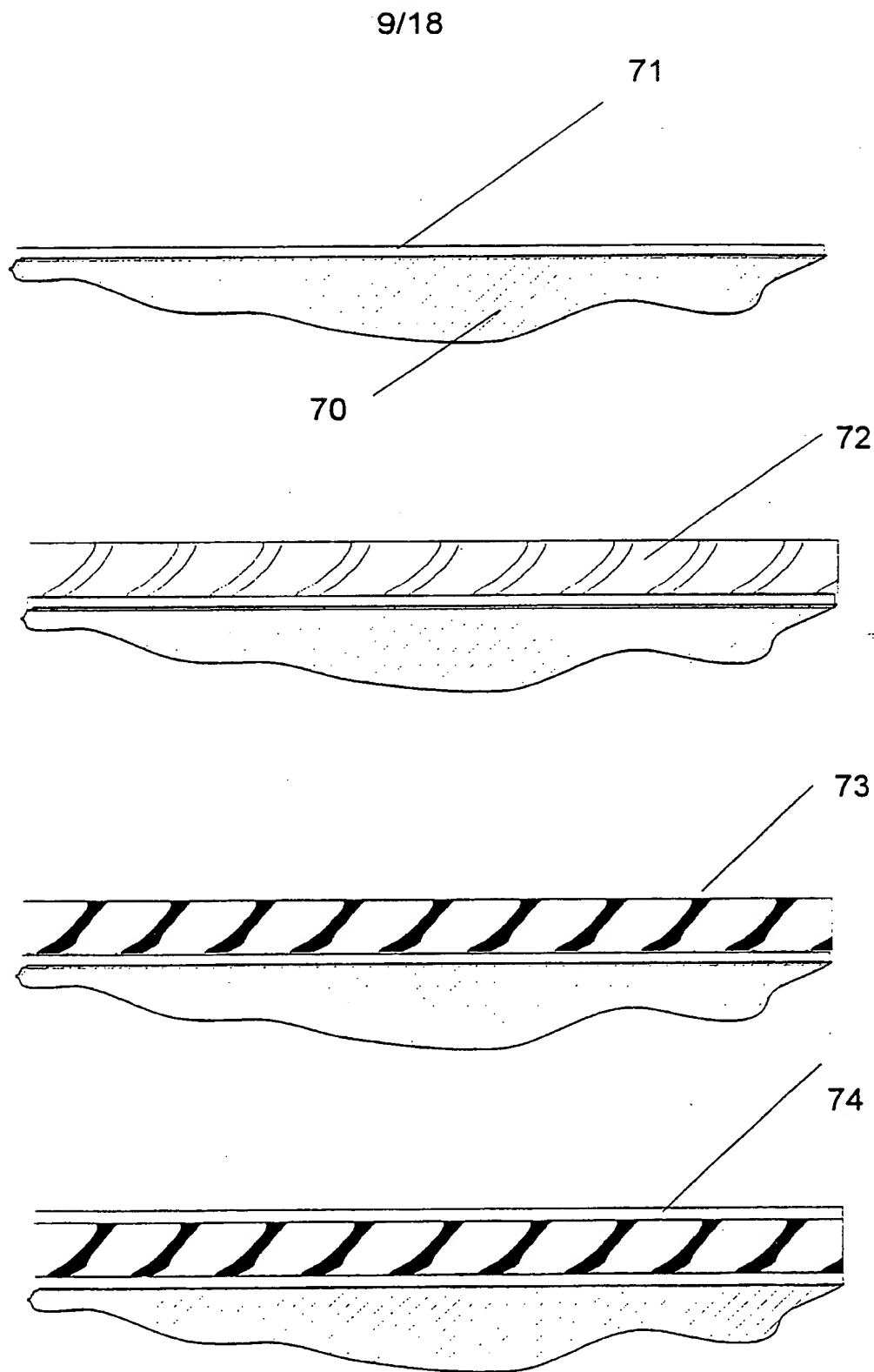


Figure 9

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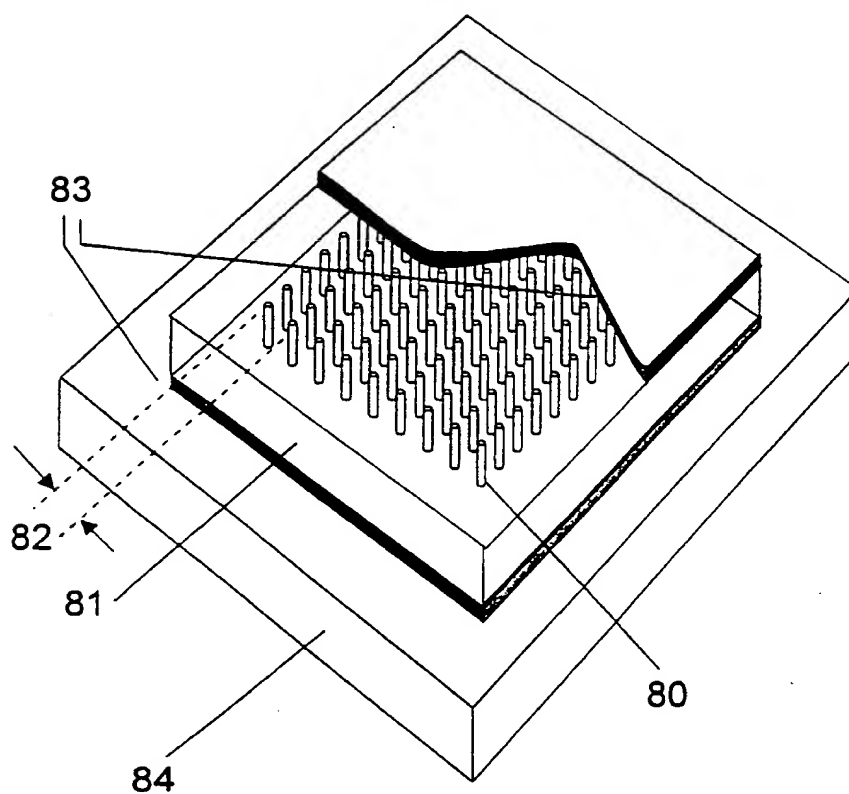


Figure 10a

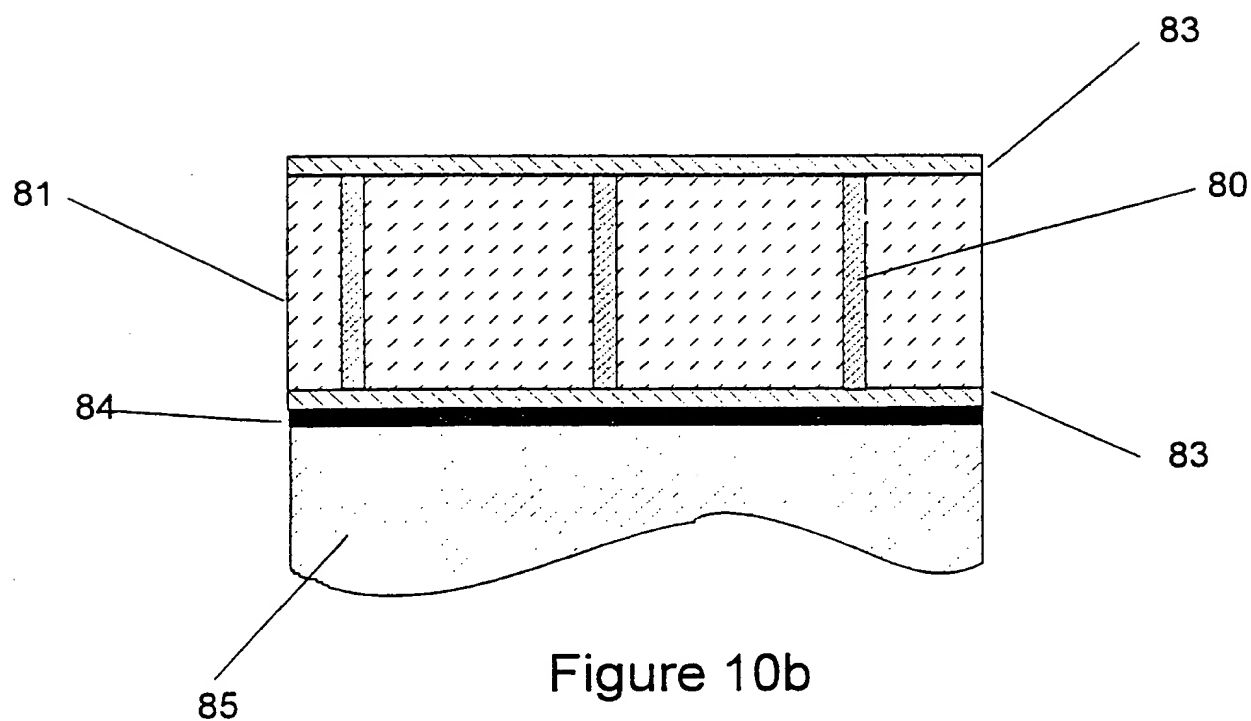


Figure 10b



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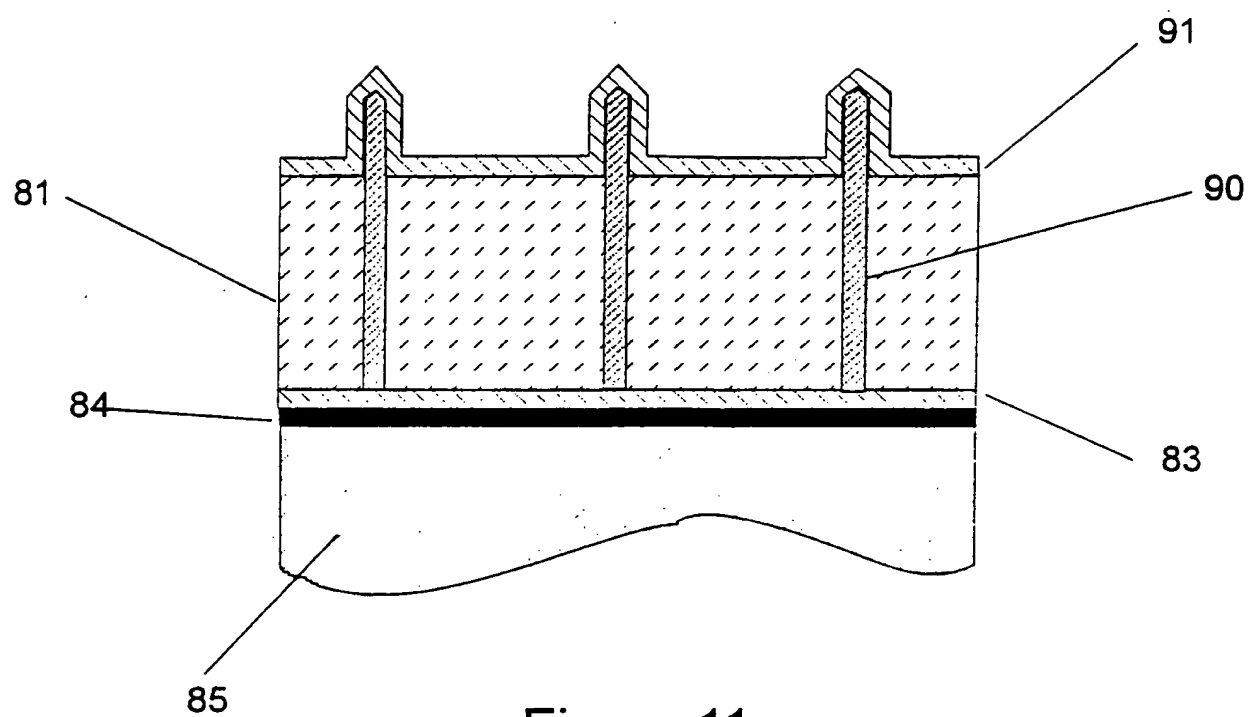


Figure 11

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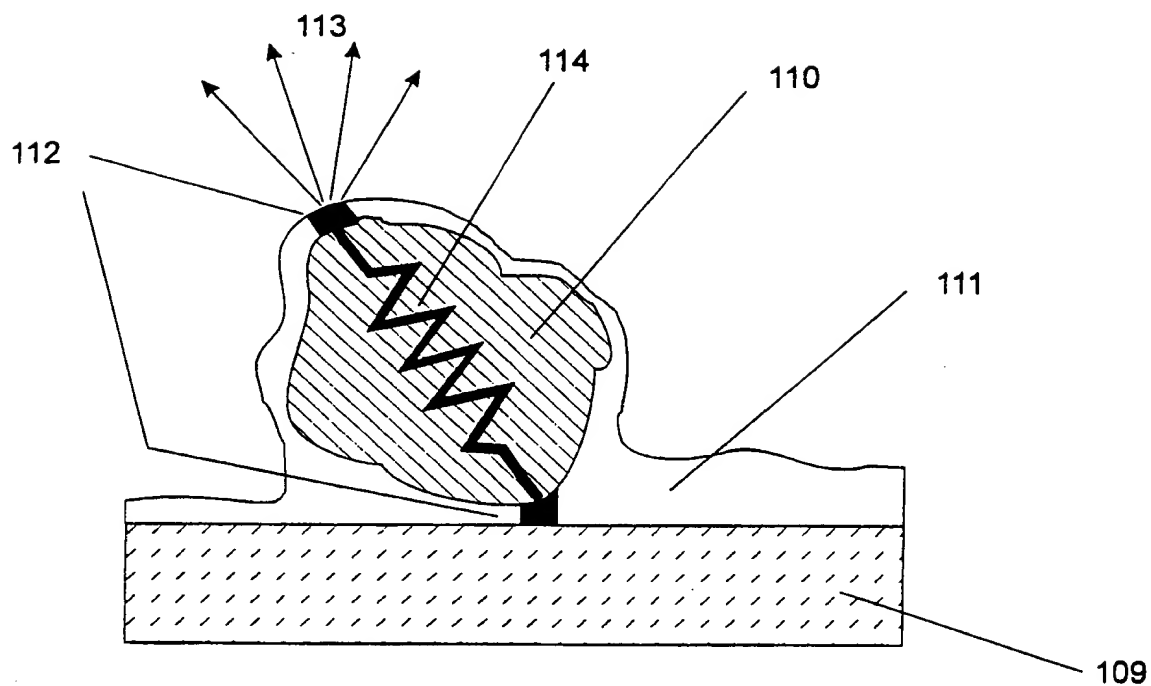


Figure 12 a

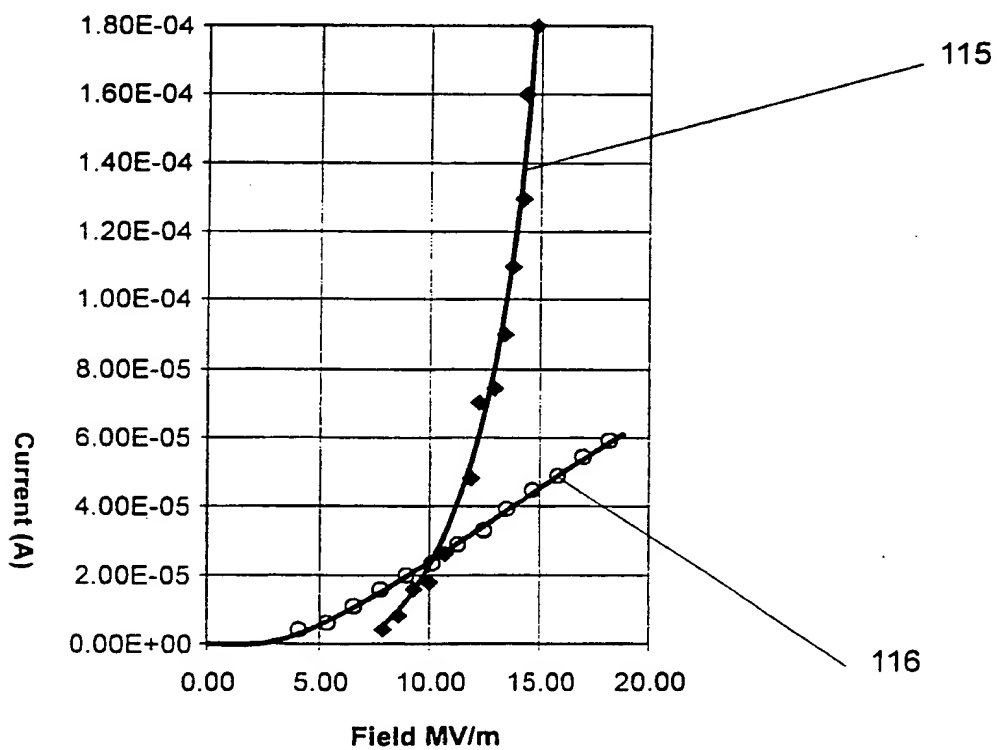


Figure 12 b

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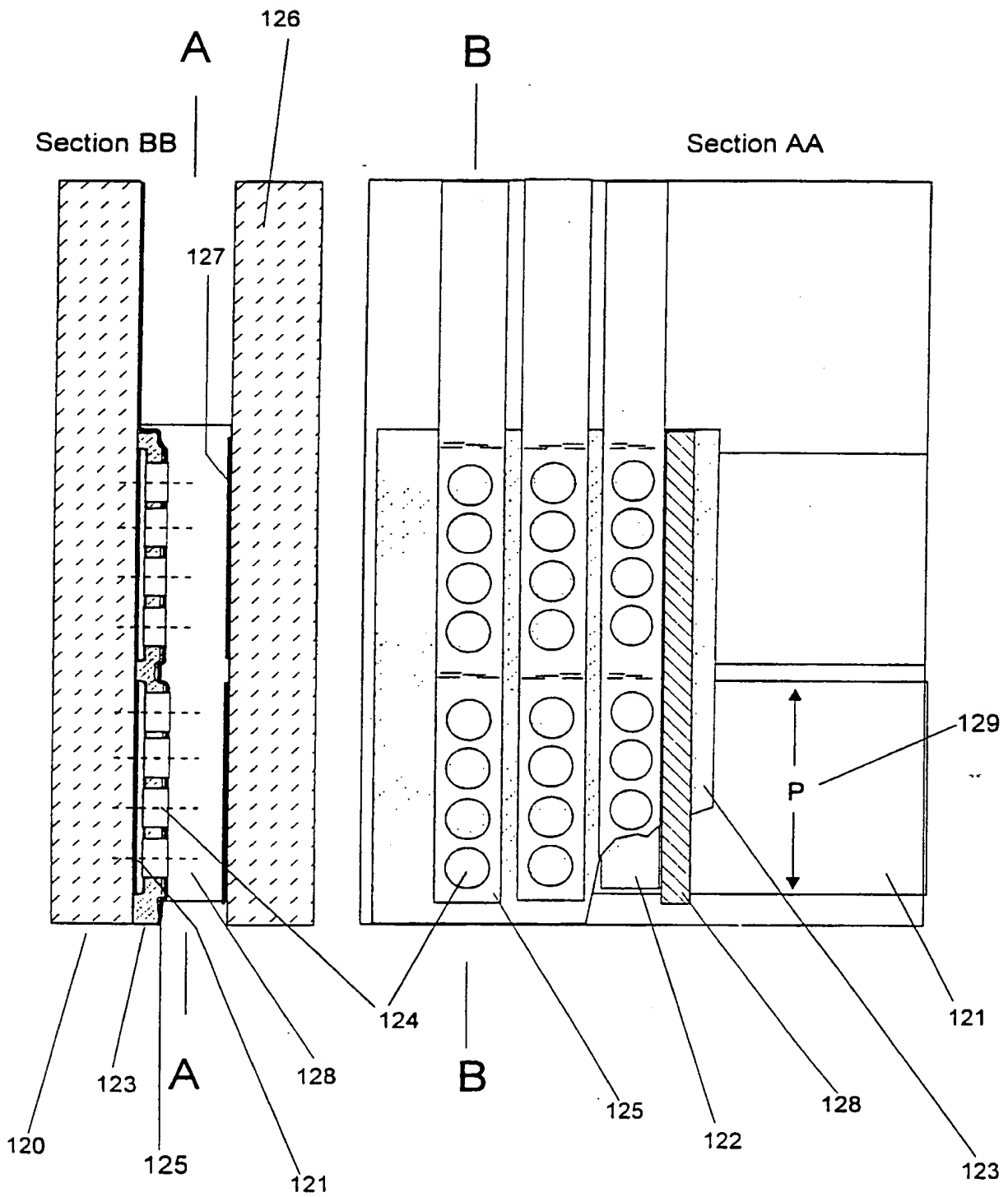


Figure 13

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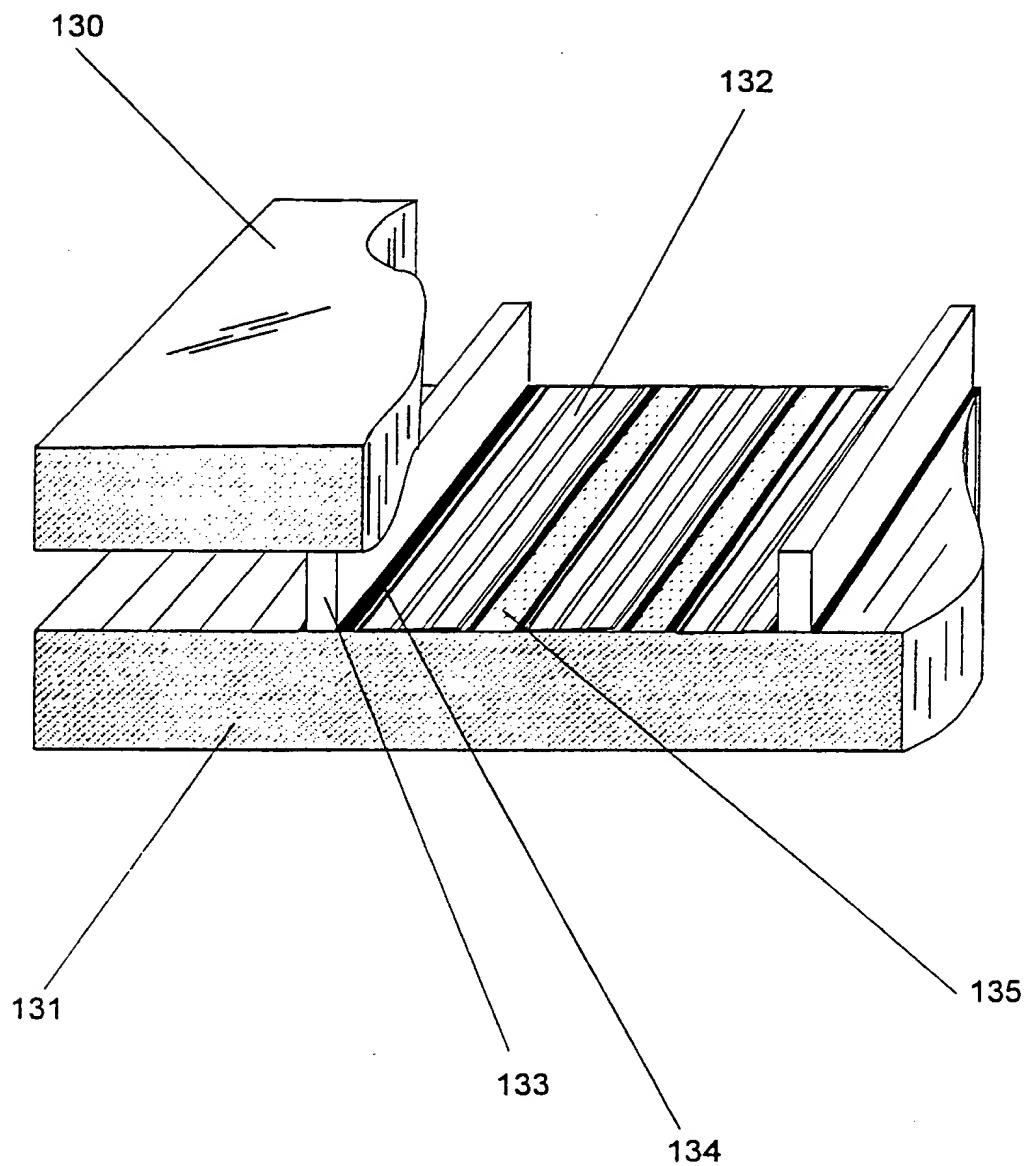


Figure 14

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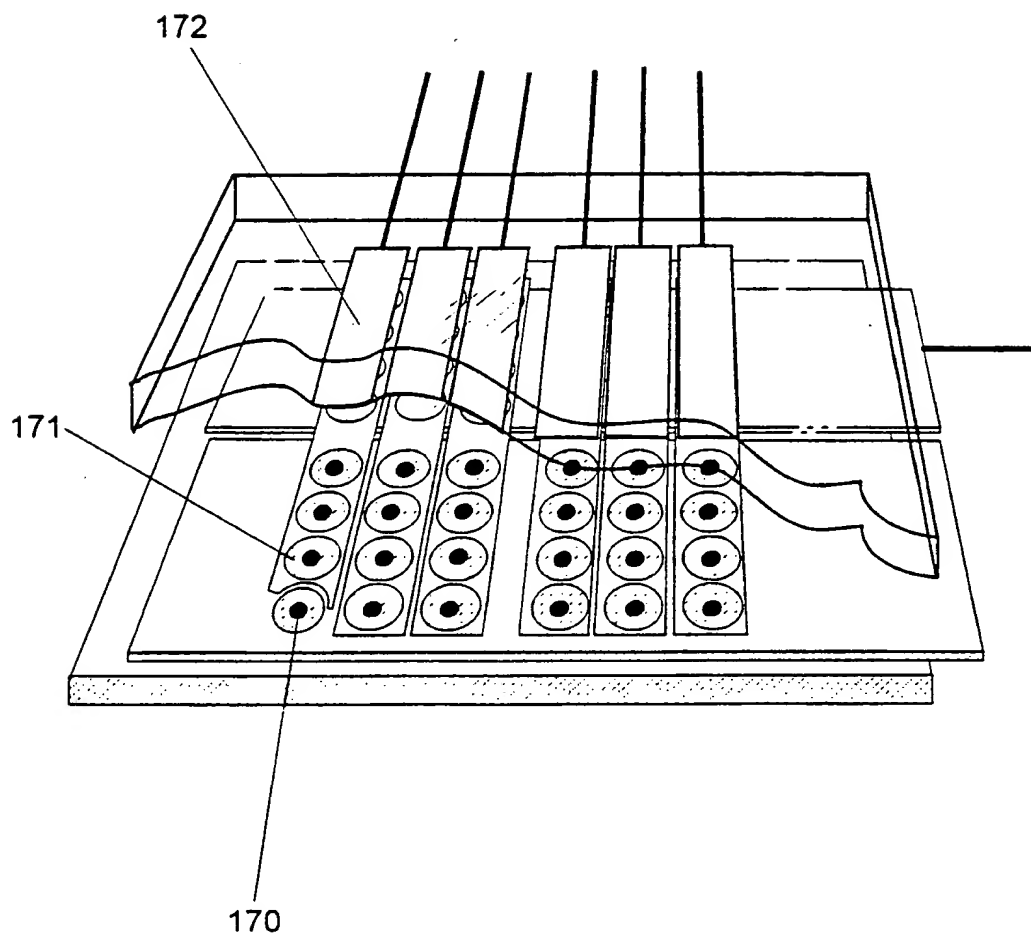


Figure 15

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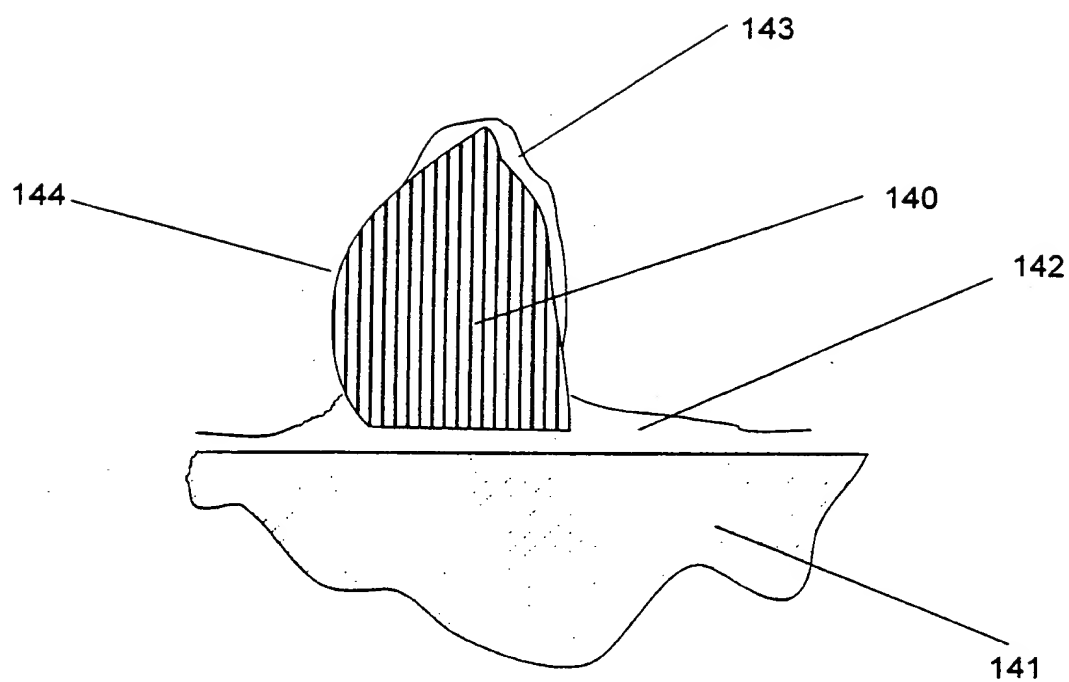
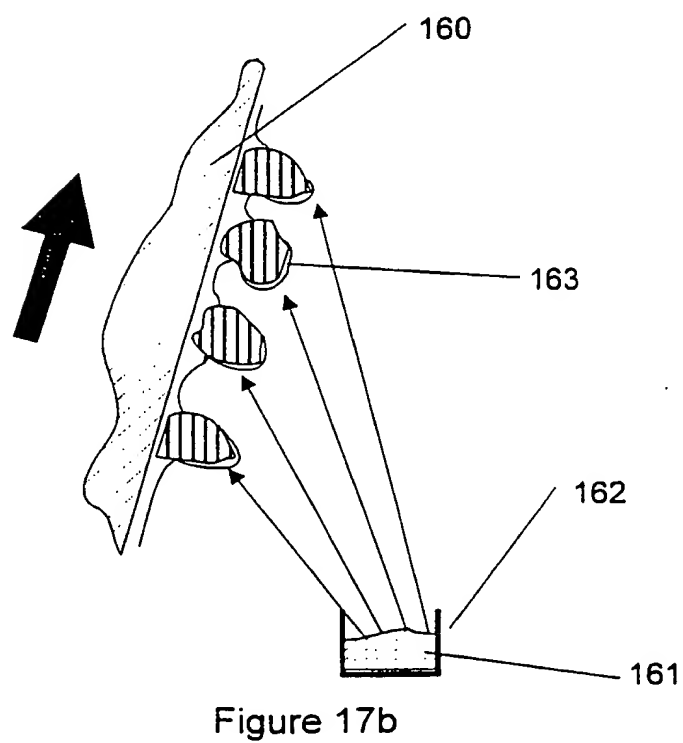
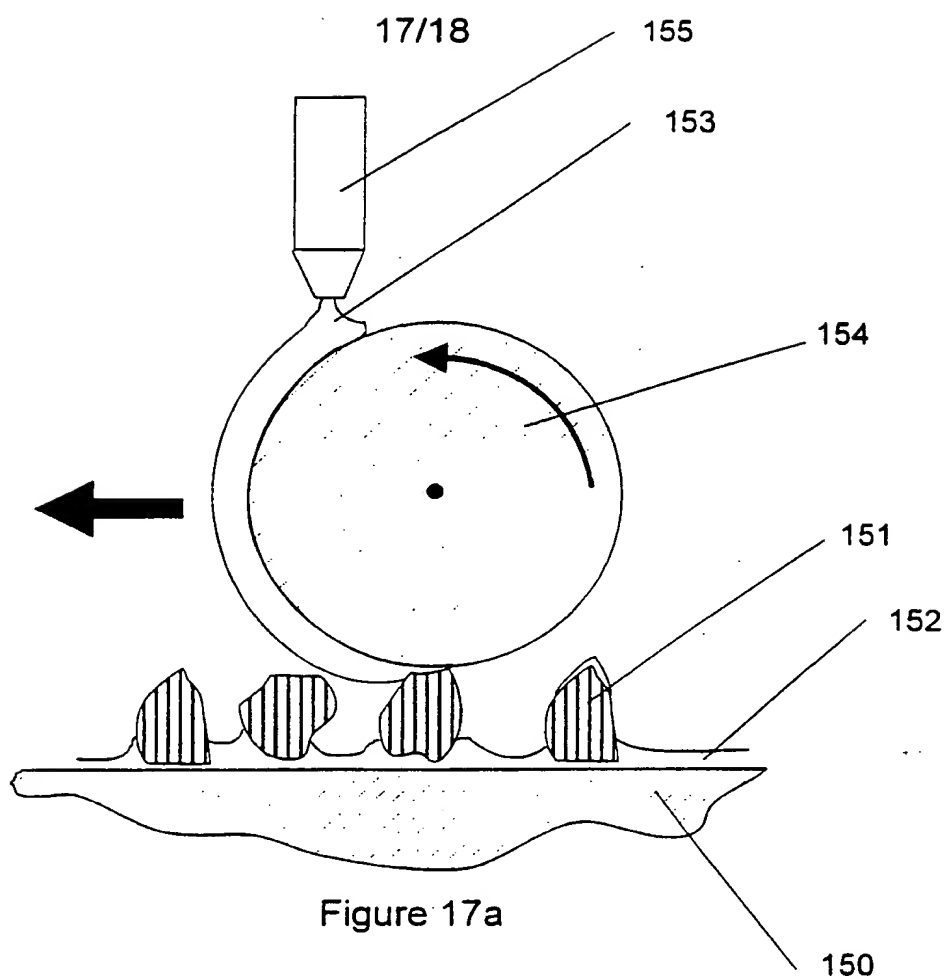


Figure 16



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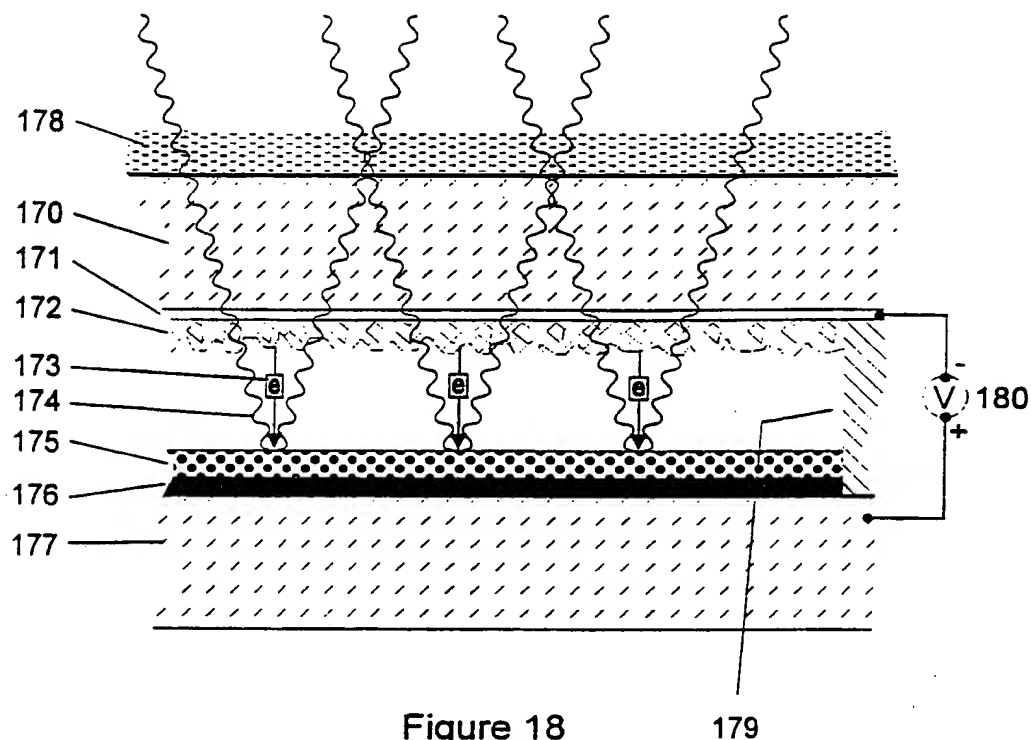


Figure 18

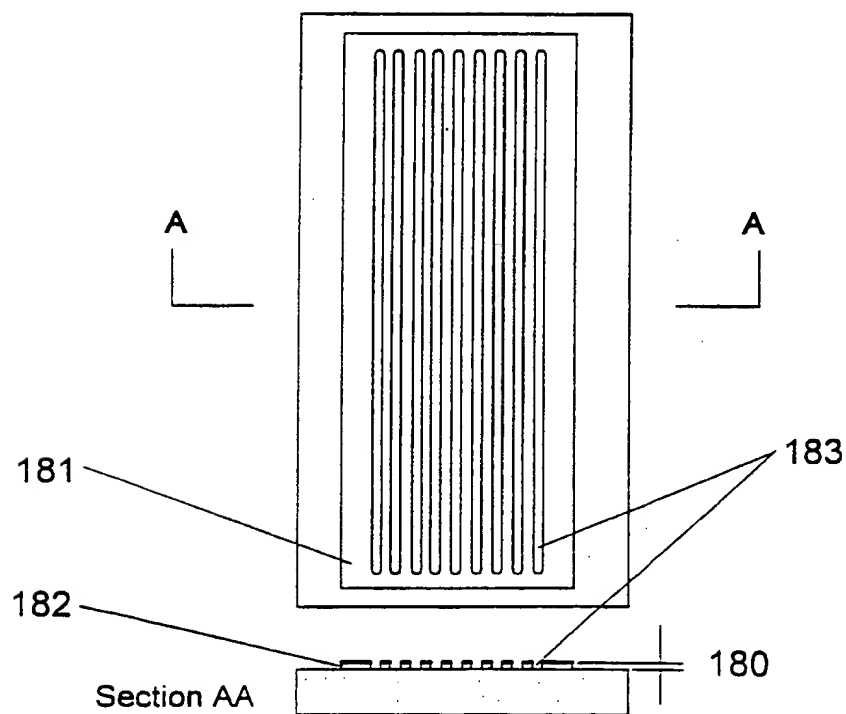


Figure 19



## INTERNATIONAL SEARCH REPORT

National Application No

GB 96/01858

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 6 H01J1/30

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 6 H01J

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JOURNAL OF PHYSICS D APPLIED PHYSICS., vol. 21, 1988, LETCHWORTH GB, pages 200-204, XP002017628 S BAJIC ET AL.: "Enhanced cold-cathode emission using composite resin-carbon coatings" cited in the application see page 202, right-hand column - page 204 ---	1-4,7, 21,32,33
Y	JOURNAL OF PHYSICS D APPLIED PHYSICS., vol. 26, 1993, LETCHWORTH GB, pages 1776-1780, XP002017629 N S XU ET AL.: "Similarities in the 'cold' electron emission characteristics of diamond coated molybdenum electrodes and polished bulk graphite surfaces" see page 1778 - page 1780 --- -/--	1-4,7, 21,32,33

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

5 November 1996

Date of mailing of the international search report

15.01.97

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+ 31-70) 340-2040, Tx. 31 651 epo nl,  
Fax (+ 31-70) 340-3016

Authorized officer

COLVIN, G

## INTERNATIONAL SEARCH REPORT

International Application No

GB 96/01858

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	ELECTRONICS LETTERS, vol. 29, no. 18, 2 September 1993, page 1596/1597 XP000395178 XU N S ET AL: "FIELD-DEPENDENCE OF THE AREA-DENSITY OF 'COLD' ELECTRON EMISSION SITES ON BROAD-AREA CVD DIAMOND FILMS" cited in the application ---	1
P,X	EP 0 709 870 A (AT & T CORP) 1 May 1996 see column 2, line 52 - column 3, line 17 see column 5, line 31 - line 43 see figures 5-9 -----	1,5,8,21

### Information on patent family members

/GB 96/01858

Form PCT/ISA:210 (patent family annex) (July 1992)

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